

[illegible]

XDI
VOA

XX		XX	DDDDDDDD	EEEEEEEEEE	LL	TTTTTTTTTT	AAAAAA		
XX		XX	DDDDDDDD	EEEEEEEEEE	LL	TTTTTTTTTT	AAAAAA		
XX		XX	DD	DD	EE		AA	AA	
XX		XX	DD	DD	EE	TT	AA	AA	
	XX	XX	DD	DD	EE	TT	AA	AA	
	XX	XX	DD	DD	EE	TT	AA	AA	
		XX	DD	DD	EEEEEEEE	TT	AA	AA	
		XX	DD	DD	EEEEEEEE	TT	AA	AA	
	XX	XX	DD	DD	EE	TT	AAAAAAAAAA		
	XX	XX	DD	DD	EE	TT	AAAAAAAAAA		
		XX	DD	DD	EE	TT	AA	AA	
		XX	DD	DD	EE	TT	AA	AA	
XX			DD	DD	EE	TT	AA	AA
XX			DD	DD	EE	TT	AA	AA
XX		XX	DDDDDDDD	EEEEEEEEEE	LLLLLLLLLL	TT	AA	AA
XX		XX	DDDDDDDD	EEEEEEEEEE	LLLLLLLLLL	TT	AA	AA

```

LL          IIIII
LL          IIIII
LL          II
LL          II
LL          II
LL          II
LL          II
LL          II
LL          II
LL          II
LL          II
LL          II
LL          II
LL          II
LLLLLLLLLLL IIIII
LLLLLLLLLLL IIIII
SSSSSSSSS
SSSSSSSSS
SS
SS
SS
SS
SSSSSS
SSSSSS
SS
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SS
SS
SSSSSSSSS
SSSSSSSSS

```

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(1)	1474	PROCESS DEBUGGER INITIALIZATION


```
0000 1  :: Version: 'V04-000'
0000 2  ::
0000 3  ::
0000 4  ::
0000 5  .MCALL MFPR
0000 6  .IF DF,SW_PROCESS
0000 7  .TITLE DELTA - MULTIMODE PROCESS DEBUGGER
0000 8  .IFF
0000 9  .TITLE XDELTA - EXECUTIVE DEBUGGER
0000 10 .ENDC
0000 11 .IDENT 'V04-000'
0000 12
0000 13 *****
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0000 32 *
0000 33 *****
0000 34 ++
0000 35 FACILITY: EXECUTIVE, DEBUGGING TOOLS
0000 36
0000 37 ABSTRACT:
0000 38 THIS MODULE PRODUCES TWO DIFFERENT DEBUGGERS DEPENDING ON THE SETTING
0000 39 OF THE ASSEMBLY SWITCH, SW_PROCESS. DELTA IS A MULTIMODE PROCESS
0000 40 DEBUGGER USING SYSTEM SERVICES WHILE XDELTA IS A STANDALONE EXEC
0000 41 DEBUGGING TOOL.
0000 42
0000 43 COMMAND SYNTAX IS IDENTICAL FOR BOTH VERSIONS EXCEPT FOR ENVIRONMENTAL
0000 44 DIFFERENCES. THE SYNTAX IS QUITE TERSE AND SOMEWHAT CRYPTIC AND
0000 45 IS DOCUMENTED IN THE "GUIDE TO WRITING AN I/O DRIVER".
0000 46
0000 47 ENVIRONMENT:
0000 48 DELTA - NORMAL PROCESS ENVIRONMENT, VARIOUS ACCESS MODES.
0000 49 XDELTA - STANDALONE, RESIDENT, KERNEL MODE, IPL=31
0000 50 BOTH VERSIONS MUST BE POSITION INDEPENDENT - BEWARE!
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0000 51 .SBTTL HISTORY ; DETAILED
0000 52 :
0000 53 : AUTHOR: R. HUSTVEDT CREATION DATE: 15-NOV-76
0000 54 :
0000 55 : REVISION HISTORY:
0000 56 :
0000 57 : V02-009 LJK0030 Lawrence J. Kenah 31-Jul-1981
0000 58 : Make changes necessary to support large physical memory
0000 59 : configurations. Change names of PFN listhead cells. Add
0000 60 : labels to cells for XE and XF stored strings to allow
0000 61 : access from INIT.
0000 62 :
0000 63 : V02-008 TCM0001 Trudy C. Matthews 29-Jul-1981
0000 64 : Change all '7ZZ's to '730's.
0000 65 :
0000 66 : V02-007 KDM0003 Kathleen D. Morse 15-Sep-1980
0000 67 : Make changes to run with multi-processor privileged program.
0000 68 :

```

```
0000 70 .SBTTL DECLARATIONS
0000 71
0000 72 :
0000 73 : INCLUDE FILES:
0000 74 :
0000 75 $ACBDEF : DEFINE AST CONTROL BLOCK
0000 76 $CADEF : DEFINE ASSEMBLY SWITCHES
0000 77 $CLIDEF : DEFINE CLI VALUES
0000 78 $IPLDEF : DEFINE IPL VALUES
0000 79 $IRPDEF : DEFINE IRP VALUES
0000 80 $PCBDEF : DEFINE PROCESS CONTROL BLOCK
0000 81 $PRDEF : DEFINE PROCESSOR REGISTERS
0000 82 $PRIDEF : DEFINE PRIORITY INCREMENT CLASSES
0000 83 $PRTDEF : DEFINE PROTECTION VALUES
0000 84 $PSLDEF : DEFINE PSL FIELDS
0000 85 $SSDEF : DEFINE SYSTEM SERVICE STATUS CODES
0000 86
0000 87 :
0000 88 : MACROS:
0000 89 :
0000 90 :
0000 91 :
0000 92 : CPU TYPE DISPATCH MACRO:
0000 93 :
0000 94 : THE ADDRESSES IN THE ADDRESS LIST ARE:
0000 95 : -ADDRESS OF CODE FOR CPU TYPE=1 (11/780)
0000 96 : -ADDRESS OF CODE FOR CPU TYPE=2 (11/750)
0000 97 : -ADDRESS OF CODE FOR CPU TYPE=3 (11/730)
0000 98 : -ADDRESS OF CODE FOR CPU TYPE=4 (?)
0000 99 : -ETC.
0000 100 :
0000 101 : CPUDISP IS INVOKED TO HANDLE CPU DIFFERENCES IN LINE. WHEN THE
0000 102 : NEXT CPU IS ADDED, ALL OCCURRENCES OF CPUDISP MUST BE EXPANDED
0000 103 : TO HANDLE FOUR CPU SPECIFIC PATHS.
0000 104 :
0000 105 .MACRO CPUDISP,ADDRLIST
0000 106 CASE G^EXE$GB_CPUTYPE,<ADDRLIST>,LIMIT=#PR$_SID_TYP780,TYPE=B
0000 107 .ENDM CPUDISP
0000 108
0000 109 :
0000 110 : EQUATED SYMBOLS:
0000 111 :
00000008 0000 112 V_F1=8 : FIELD 1 PRESENT FLAG
00000009 0000 113 V_F2=9 : FIELD 2 PRESENT FLAG
0000000A 0000 114 V_F3=10 : FIELD 3 PRESENT FLAG
0000000B 0000 115 V_F4=11 : FIELD 4 PRESENT FLAG
0000000C 0000 116 V_F5=12 : FIELD 5 PRESENT FLAG
0000 117 :
00000000 0000 118 V_OPEN=0 : OPEN CELL FLAG
00000001 0000 119 V_ASCII=1 : ASCII
00000002 0000 120 V_INFIELD=2 : FIELD IN PROGRESS
00000003 0000 121 V_TBIT=3 : ENABLE TBIT
00000004 0000 122 V_ATBRK=4 : AT BREAKPOINT
00000005 0000 123 V_TBITOK=5 : TBIT EXPECTED
00000006 0000 124 V_RUB=6 : RUBOUT IN PROGRESS
00000007 0000 125 V_NEGATE=7 : NEGATE BIT
0000000F 0000 126 V_PMODE=15 : PROCESSOR REGISTER MODE
```



```
0000001F 0000 127 V_PREG=31 ; PROCESSOR REGISTER FLAG
00000000 0000 128 ;
00000000 0000 129 RDCR=0 ; READ CSR
00000002 0000 130 RDBUF=2 ; READ BUFFER
00000004 0000 131 OUTCR=4 ; OUTPUT CSR
00000006 0000 132 OUTB=6 ; OUTPUT BUFFER
00000000 0000 133 ;
0000005C 0000 134 BSLSH=92 ; BACK SLASH CODE
0000000D 0000 135 CR=13 ; CARRIAGE RETURN
0000000A 0000 136 LF=10 ; LINE FEED
00000027 0000 137 QUOT=39 ; QUOTE
0000007F 0000 138 RUBOUT=127 ; RUBOUT CODE
0000002F 0000 139 SLSH=47 ; SLASH CODE
00000000 0000 140 ;
00000000 0000 141 ;
00000000 0000 142 ;
00000000 0000 143 ;
00000000 0000 144 ;
00000000 0000 145 ;
00000000 0000 146 .LIST MEB ; DISPLAY MACRO EXPANSIONS
00000000 0000 147 .IF DF,SW_PROCESS
00000000 0000 148 .PSECT DELTA, LONG
00000000 0000 149 DELBASE: .LONG DELBASE-DELBASE ; RELATIVE PAGE NUMBER OF WRITABLE
00000000 0000 150 .LONG <511+DELEND-DELBASE>&^C511 ; REL PAGE NUMBER OF END OF WRITABLE
00000000 0000 151 .LONG DELTA_START-DELBASE ; START ADDRESS
00000000 0000 152 .IFF
00000000 0000 153 .PSECT Z$DEBUGXDELTA, LONG
00000000 0000 154 .ENDC
00000000 0000 155 CONTEXT: ;
00000000 0000 156 .LONG 0 ; BUFFER PADDING
00000034 0004 157 INBUF: .BLKB 48 ; INPUT BUFFER
00000000 0034 158 STATUS: .LONG 0 ; STATUS FLAGS
00000000 0038 159 F1: .LONG 0 ; FIELDS
00000000 003C 160 F2: .LONG 0 ; 0-7
00000000 0040 161 F3: .LONG 0 ;
00000000 0044 162 F4: .LONG 0 ;
00000000 0048 163 F5: .LONG 0 ;
00000000 004C 164 ;
00000000 004C 165 MFYFLG: .LONG 0 ; MODIFY ENABLE FLAG FOR OTHER PROCESS
00000000 0050 166 ; ADDRESS SPACES
00000000 0050 167 PID: .LONG 0 ; PID FOR ADDRESS SPACE 0=>SELF
00000000 0054 168 ;
00000000 0054 169 FCTR: .BYTE 0 ; FIELD COUNTER
00000000 0055 170 ;
00000000 0055 171 DTYPE: .BYTE 2 ; DATA TYPE
00000000 0056 172 CURTYPE: .BYTE 2 ; CURRENT TYPE
00000000 0057 173 ;
00000000 0057 174 OPER: .BYTE 0 ; OPERATOR
00000000 0058 175 B: ; BASE OF DATA AREA(CENTER)
00000000 0058 176 CURDOT: .LONG 0 ; CURRENT LOCATION
00000000 005C 177 QUAN: .LONG 0 ; QUANTITY (:Q)
00000070 0060 178 OUTBUF: .BLKL 4 ; OUTPUT BUFFER
00000000 0070 179 ;
00000000 0070 180 ; REGISTER SAVE AREA
00000000 0070 181 ;
00000000 0070 182 SAVREG: ; REGISTER SAVE AREA
00000074 0070 183 .BLKL 1 ; R0
```

```
00000078 0074 184 .BLKL 1 ; R1
0000007C 0078 185 SAVR2: .BLKL 1 ; R2
00000080 007C 186 .BLKL 1 ; R3
00000084 0080 187 .BLKL 1 ; R4
00000088 0084 188 .BLKL 1 ; R5
0000008C 0088 189 .BLKL 1 ; R6
00000090 008C 190 .BLKL 1 ; R7
00000094 0090 191 .BLKL 1 ; R8
00000098 0094 192 .BLKL 1 ; R9
0000009C 0098 193 .BLKL 1 ; R10
000000A0 009C 194 .BLKL 1 ; R11
000000A4 00A0 195 SAVAP: .BLKL 1 ; AP
000000A8 00A4 196 .BLKL 1 ; (FP)
000000AC 00A8 197 SAVSP: .BLKL 1 ; SP
000000B0 00AC 198 SAVPC: .BLKL 1 ; PC
000000B4 00B0 199 SAVPSL: .BLKL 1 ; PSL
000000B6 00B4 200 SAVOCR: .BLKW 1 ; OUTPUT CSR SAVE
000000B8 00B6 201 SAVRCR: .BLKW 1 ; INPUT CSR SAVE
000000B8 00B8 202 ASTEN: ; AST ENABLE SAVE LOCATION
000000BC 00B8 203 SAVRXCS: .BLKL 1 ; CONSOLE RECEIVER STATUS
000000BC 00BC 204 ;
000000BC 00BC 205 CONTEXTSZ=-CONTEXT ; SIZE OF PER MODE CONTEXT AREA
000000BC 00BC 206 ;
000000BC 00BC 207 ; RESERVE SPACE FOR MULTIPLE MODE CONTEXT AREA
000000BC 00BC 208 ;
000000BC 00BC 209 .IF DF,SW_PROCESS ;
000000BC 00BC 210 .REPT 3 ;
000000BC 00BC 211 .BLKB CONTEXTSZ ; FOR EXEC,SUPER AND USER
000000BC 00BC 212 SAV...= ;
000000BC 00BC 213 .=-CONTEXTSZ+<DTYPE-CONTEXT> ; POINT AT DTYPE,CURTYP
000000BC 00BC 214 .BYTE 2,2 ; SET TYPE TO LONGWORD
000000BC 00BC 215 .=SAV... ; RESTORE LOCATION COUNTER
000000BC 00BC 216 .ENDR ;
000000BC 00BC 217 .ENDC ;
000000BC 00BC 218 ;
000000BC 00BC 219 ;
000000BC 00BC 220 ;
000000BC 00BC 221 ; BREAK POINT DATA
000000BC 00BC 222 ;
000000B8 00BC 223 BRKADR=-4 ;
000000BC 00BC 224 .IF NDF,SW_PROCESS ;
00000000' 00BC 225 XDELIBRK:: ;
00000000' 00C0 226 .LONG INISBRK ; ADDRESS OF INITIAL BREAKPOINT
00000000' 00C0 227 .IFF ; FOR PROCESS VERSION
00000000' 00C0 228 INIBRKA: .LONG 0 ; INITIAL BREAKPOINT
00000000' 00C0 229 .ENDC ;
000000DC 00C0 230 .BLKL 7 ; OTHER BREAK POINT ADDRESSES
00000008 00DC 231 NBRK=<.-4-BRKADR>/4 ; NUMBER OF BREAKPOINTS
00000008 00DC 232 BRKOP=-1 ; SAVED OPCODE
00000001 00DC 233 NOP ; INITIAL OPCODE
000000E4 00DD 234 .BLKB 7 ; REMAINING OPCODES
000000E4 00E4 235 ;
000000E4 00E4 236 ;
000000E0 00E4 237 BRKDSP=-4 ;
00000104 00E4 238 .BLKL 8 ; DISPLAY LOCATION START
00000100 0104 239 BRKCOM=-4 ;
00000124 0104 240 .BLKL 8 ; COMMAND START
```



```
00000130 0124 241
0124 242 XREGV: .BLKL 3
0130 243 XDEL_LOADBASE::
00000000 0130 244 .LONG 0
00000000 0134 245 SCH$GL_CURPCB
00000000 0138 246 SCH$GL_PCBVEC
013C 247 NDF_SW_PROCESS
00000000 013C 248 PFN$AW_SWPVBN
00000000 0140 249 PFN$AL_PTE
00000000 0144 250 PFN$AL_BAK
00000000 0148 251 PFN$AW_REFCNT
00000000 014C 252 PFN$A_FLINK
00000000 0150 253 PFN$A_BLINK
00000000 0154 254 PFN$AB_STATE
00000000 0158 255 PFN$AB_TYPE
015C 256 XDS$GL_XESTRING::
00000000 015C 257 XDS$GT_WORD_PFN
0160 258 XDS$GL_XFSTRING::
00000000 0160 259 XDS$GT_WORD_PFN
00000168 0164 260 MCHKSAV: .BLKL 1
0168 261 .IFF
0168 262 .BLKL 10
0168 263 TTIO$B: .BLKL 2
0168 264 TTCHAN: .BLKL 1
0168 265 TTNAME: .LONG 2, TTSTR
0168 266 TTSTR: .ASCII /TT/
0168 267 DBGACTIVE:
0168 268 .LONG 0
0168 269 EXITBLK:
0168 270 .LONG 0
0168 271 EXIHADR: .LONG EXIHANDLE
0168 272 .LONG 1
0168 273 EXCODA: .LONG EXITCODE
0168 274 EXITCODE:
0168 275 .LONG 1
0168 276 KCOND: .LONG 0
0168 277 ECOND: .LONG 0
0168 278 SCOND: .LONG 0
0168 279 TERMASKD:
0168 280 .LONG 16
0168 281 TERMASK
0168 282 TERMASK: .LONG <1a9>!<1a10>!<1a13>!<1a27>
0168 283 .LONG <1a2>!<1a15>!<1a29>
0168 284 .LONG <1a19>
0168 285 .LONG 0
0168 286
0168 287 .ENDC
```

```
: X REGISTER VECTOR
: BASE OF LOADABLE
: CPU DEPENDENT CODE
: X4 = CURRENT PCB ADDRESS
: X5 = BASE OF PCB VECTOR
:
: X6 = SWAP VBN
: X7 = PTE BACK POINTER
: X8 = BACKUP ADDRESS
: X9 = REFERENCE COUNT
: XA = FORWARD LINK
: XB = BACK LINK
: XC = STATE
: XD = TYPE
:
: XE;E WITH X0 = PFN , DEFAULT TO WORD ARRAY
:
: XF;E WITH R0 = PFN , DEFAULT TO WORD ARRAY
: SAVED CONTENT OF MACHINE CHECK VECTOR
: FOR PROCESS VERSION
:
: IO STATUS BLOCK FOR TERMINAL READ
: CHANNEL NUMBER
: ACTUAL ADDRESS FOR DESCR SET BY INIT
:
: ACTIVE FLAGS BY ACCESS MODE
:
: EXIT HANDLER BLOCK
:
: EXIT HANDLER
: ARGUMENT COUNT
: ADDRESS TO STORE EXIT CODE
:
: RECEIVER FOR EXIT CODE
: PREVIOUS KERNEL HANDLER
: PREVIOUS EXEC HANDLER
: PREVIOUS SUPER HANDLER
: TERMINATOR MASK DESCRIPTOR
: MASK LENGTH
: MASK ADDRESS
: : TAB,LF,CR,ESC
: DOUBLE QUOTE,SLASH,EQUALS
: 'S'
```

		0168	289	.SBTTL	PRIMARY COMMAND CHARACTER SWITCH	:
		0168	290			
		0168	291	:		
		0168	292	PRIMARY CHARACTER LIST		
		0168	293	:		
		0168	294	PRIMARY:		:
42 41 39 38 37 36 35 34 33 32 31 30		0168	295	.ASCII /0123456789ABCDEF/	: DECIMAL AND HEX CHARS	
46 45 44 43		0174				
	2E	0178	296	.ASCII /. /	: DOT - CURRENT LOCATION	
	2C	0179	297	.ASCII /./	: COMMA - FIELD SEPARATOR	
000000	12	017A	298	OPERBAS=-PRIMARY	: OPERATORS	
	2B	017A	299	.ASCII /+ /	: PLUS - ADD	
	20	017B	300	.ASCII / /	: BLANK - SAME AS PLUS	
	40	017C	301	.ASCII /@ /	: SHIFT OPERATOR	
	2A	017D	302	.ASCII /* /	: MULTIPLY OPERATOR	
	25	017E	303	.ASCII /% /	: DIVIDE OPERATOR	
	2D	017F	304	.ASCII /- /	: MINUS - SUBTRACT OPERATOR	
	5B	0180	305	.ASCII /[/	: LBRACKET - LEFT BRACKET	
		0181	306	TERM:	: BASE OF TERMINATOR LIST	
	09	0181	307	.ASCII <9>	: TAB - INDIRECT	
	0A	0182	308	.ASCII <10>	: LINEFEED -	
	0D	0183	309	.ASCII <CR>	: RETURN -	
	2F	0184	310	.ASCII '/'	: SLASH - OPEN FOR DISPLAY	
	22	0185	311	.ASCII '"'	: DOUBLE QUOTE - OPEN FOR ASCII DISPLAY	
	3D	0186	312	.ASCII /= /	: EQUALS - DISPLAY	
	1B	0187	313	.ASCII <27>	: ESCAPE - PREVIOUS LOCATION	
	53	0188	314	.ASCII /S /	: STEP	
000000	08	0189	315	NTERM=-TERM	: NUMBER OF TERMINATORS	
	3B	0189	316	.ASCII <59>	: SEMI - INITIATE SECONDARY	
	3A	018A	317	.ASCII /:/	: COLON - SEPARATE PID FORM ADDRESS	
	50	018B	318	.ASCII /P /	: P - PROCESSOR REGISTER PREFIX	
	51	018C	319	.ASCII /Q /	: Q - LAST QUANTITY	
	27	018D	320	.ASCII /\ /	: QUOTE - BEGIN CHAR STRING	
	52	018E	321	.ASCII /R /	: REGISTER PREFIX	
	47	018F	322	.ASCII /G /	: G - GLOBAL PREFIX	
	48	0190	323	.ASCII /H /	: H - HIGH, P1 SPACE PREFIX	
	58	0191	324	.ASCII /X /	: X REGISTER PREFIX	
000000	2A	0192	325	NPRIM=-PRIMARY	: NUMBER OF PRIMARY COMMANDS	
		0192	326			

00	0D	0A	3F	4B	45	0D	0A	0192	328	.SBTTL	PRIMARY COMMAND SCANNER	
								0192	329			
								0192	330			
								0192	331	PRIMARY COMMAND SCANNER		
								0192	332			
								0192	333			
								0192	334			
								0192	335	OUTER:	.ASCIZ	<LF><CR>/EH?/<LF><CR>
								019A	336			
								019A	337	DCOM:	.WORD	CALL ENTRY POINT
								019C	338		.IF	DF,SW PROCESS
								019C	339		MOVAB	W*DBGEXCEP,(FP)
								019C	340		.ENDC	SET CONDITION HANDLER ADDRESS
								019C	341		BRB	ENTER SCANP
54	F1	AF	9E	01D9	30	01A2	342	019E	342	ERROR:	MOVAB	OUTER,R4
							343		343		BSBW	OUTZSTRING
59	5E	5D	D0	01A5	344	01A5	344	01A5	344	SUPERST:	MOVL	FP,SP
							345		345		MOVAB	INBUF-B(R11),R9
							346		346		CLRB	(R9)
							347		347		BSBW	RESET
							348		348	SCANP:	BSBB	NEXTP
							349		349		BRB	SCANP
							350		350	NEXTP:		
							351		351		BSBW	GETCHAR
AB	AF	2A	58	0206	30	01B5	351	01B5	351		LOCC	R8,#NPRIM,PRIMARY
							352		352		BEQL	ERROR
							353		353		SUBL3	R0,#NPRIM,R0
50	2A	50	C3	01BF	354	01BF	354	01BF	354		CASE	R0,LIMIT=#16,<-
							355		355			DOT,-
							356		356			COMMA,-
							357		357			OPERATOR,-
							358		358			PLUS - ADD OPERATOR
							359		359			BLANK - ADD OPERATOR
							360		360			@ - SHIFT OPERATOR
							361		361			* - MULTIPLY OPERATOR
							362		362			% - DIVIDE OPERATOR
							363		363			MINUS - SUBTRACT/NEGATE
							364		364			LEFT BRACKET - MODE SELECT
							365		365			TAB - INDIRECT
							366		366			LINE FEED - NEXT LOCATION
							367		367			RETURN - CLOSE OPEN CELL
							368		368			SLASH - OPEN FOR DISPLAY
							369		369			DOUBLE QUOTE - OPEN FOR ASCII DISPLAY
							370		370			EQUALS - DISPLAY VALUE
							371		371			ESCAPE - PREVIOUS LOCATION
							372		372			'S' - SINGLE STEP
							373		373			SEMI COLON - SECONDARY COMMAND
							374		374			COLON - SEPARATE PID FROM ADDRESS
							375		375			'P' - PROCESSOR REGISTER
							376		376			'Q' - LAST QUANTITY
							377		377			QUOTE - BEGIN ASCII STRING
							378		378			
							379		379			G - GLOBAL PREFIX
							380		380			H - P1 SPACE PREFIX


```
03C8: 01C7 .SIGNED_WORD DOT-30000$
00D4: 01C9 .SIGNED_WORD COMMA-30000$
026A: 01CB .SIGNED_WORD OPERATOR-30000$
026A: 01CD .SIGNED_WORD OPERATOR-30000$
026A: 01CF .SIGNED_WORD OPERATOR-30000$
026A: 01D1 .SIGNED_WORD OPERATOR-30000$
026A: 01D3 .SIGNED_WORD OPERATOR-30000$
0273: 01D5 .SIGNED_WORD NEGATE-30000$
02EB: 01D7 .SIGNED_WORD LBRACKET-30000$
027F: 01D9 .SIGNED_WORD TAB-30000$
012B: 01DB .SIGNED_WORD LINEFEED-30000$
00BD: 01DD .SIGNED_WORD RETURN-30000$
008C: 01DF .SIGNED_WORD SLASH-30000$
0087: 01E1 .SIGNED_WORD DQUOTE-30000$
02A1: 01E3 .SIGNED_WORD EQUALS-30000$
028F: 01E5 .SIGNED_WORD ESCAP-30000$
0306: 01E7 .SIGNED_WORD STEP-30000$
02C4: 01E9 .SIGNED_WORD SEMI-30000$
0388: 01EB .SIGNED_WORD COLON-30000$
065E: 01ED .SIGNED_WORD PREG-30000$
03D5: 01EF .SIGNED_WORD QUANT-30000$
0616: 01F1 .SIGNED_WORD QUOTE-30000$
03ED: 01F3 .SIGNED_WORD REGISTER-30000$
0043: 01F5 .SIGNED_WORD GLOBL-30000$
0049: 01F7 .SIGNED_WORD HIGH-30000$
041B: 01F9 .SIGNED_WORD XREG-30000$
      01FB 30001$:
10 50 B1 01FB 383 CMPW R0,#16 ; IS NUMBER > RADIX
      9E 18 01FE 384 BGEQ ERROR ; YES
56 10 C4 0200 385 MULL #16,R6 ; SCALE BY RADIX
56 50 C0 0203 386 ADDL R0,R6 ; AND ADD NEW DIGIT
6A 04 C8 0206 387 INFLD: BISL #(<12V_INFIELD>),(R10) ; NOTE FIELD INPUT
      05 0209 388 RSB ; NEXT PRIMARY CHARACTER
      020A 389
      020A 390
54 01 1F 9C 020A 391 GLOBL: ROTL #31,#1,R4 ; GENERATE SYSTEM SPACE PREFIX
      07 11 020E 392 BRB PRE1 ; MERGE WITH COMMON
54 7FFE0000 8F D0 0210 393 HIGH: MOVL #^X7FFE0000,R4 ; P1 SPACE BASE ADDRESS
      06 10 0217 394 PRE1: BSBB ENDEXPR ; END EXPRESSION
56 54 D0 0219 395 MOVL R4,R6 ; SET INTO ACCUM
      E7 AF 9F 021C 396 PUSHAB INFLD ; RETURN THROUGH INFLD
      021F 397 : BRB ENDEXPR
      021F 398
```

```
021F 400 .SBTTL ENDEXPR - END EXPRESSION
021F 401
021F 402
021F 403
021F 404
021F 405 ENDEXPR:
021F 406 BBCC #V_NEGATE,(R10),5$ : SKIP IF NOT NEGATE
0223 407 MNEGL R6-R6 : NEGATE ACCUMULATOR
0226 408 5$: BSBB 10$ : PERFORM OPERATION
0228 409 CLRL R6 : CLEAR ACCUMULATOR
022A 410 CLRB OPER-B(R11) : INIT OPERATOR
022D 411 RSB : AND RETURN
022E 412 10$: CASE OPER-B(R11),TYPE=B,<- : DO OPERATION
022E 413 ADD,- : ADD, PLUS
022E 414 ADD,- : BLANK, PLUS
022E 415 SHFT,- : SHIFT, @
022E 416 MUL,- : MULTIPLY, *
022E 417 DIV,- : DIVIDE, %
022E 418 >
04' 00 FF AB 8F 022E CASEB OPER-B(R11),#0,S^#<<30003$-30002$>/2>-1
0233 30002$:
0017' 0233 .SIGNED_WORD ADD-30002$
0017' 0235 .SIGNED_WORD ADD-30002$
000A' 0237 .SIGNED_WORD SHFT-30002$
000F' 0239 .SIGNED_WORD MUL-30002$
0013' 023B .SIGNED_WORD DIV-30002$
023D 30003$:
57 57 56 78 023D 419 SHFT: ASHL R6,R7,R7 : SHIFT
05 0241 420 RSB : AND EXIT
57 56 C4 0242 421 MUL: MULL R6,R7 : MULTIPLY
05 0245 422 RSB : AND EXIT
57 56 C6 0246 423 DIV: DIVL R6,R7 : DIVIDE
05 0249 424 RSB : AND EXIT
57 56 C0 024A 425 ADD: ADDL R6,R7 : ADD
05 024D 426 RSB : AND EXIT
024E 427
```

```
024E 429 .SBTTL SLASH - OPEN CELL
024E 430
024E 431 :
024E 432 :
024E 433 :
024E 434 :
024E 435 :
0251 436 :
0253 437 :
0253 438 :
0253 439 :
0256 440 :
0258 441 :
025C 442 :
0260 443 :
0262 444 :
0266 445 :
026B 446 :
0270 447 :
0273 448 :
0277 449 :
027B 450 :
027D 451 :
027D 452 :
027F 453 :
027F 454 :
027F 455 :
027F 456 :
0281 457 :
0284 458 :

6A 02 88 024E 435 :
03 11 0251 436 :
03 11 0253 437 :
03 11 0253 438 :
03 11 0253 439 :
03 11 0256 440 :
03 11 0258 441 :
03 11 025C 442 :
03 11 0260 443 :
03 11 0262 444 :
03 11 0266 445 :
03 11 026B 446 :
03 11 0270 447 :
03 11 0273 448 :
03 11 0277 449 :
03 11 027B 450 :
03 11 027D 451 :
03 11 027D 452 :
03 11 027F 453 :
03 11 027F 454 :
03 11 027F 455 :
03 11 027F 456 :
03 11 0281 457 :
03 11 0284 458 :

06 6A 08 E0 0258 441 :
6B 04 AB D0 025C 442 :
6B E0 AB D0 0262 444 :
6B 01 0F EF 0266 445 :
6A 01 1F 50 F0 026B 446 :
0086 30 0270 447 :
1A 6A 09 E1 0273 448 :
6B E4 AB D1 0277 449 :
14 15 027B 450 :
76 10 027D 451 :
027D 452 :
027F 453 :
027F 454 :
027F 455 :
F6 11 027F 456 :
FF1A 31 0281 457 :
0284 458 :

SLASH:
OPEN:
5$:
10$:
15$:
ERR4:

BISB #<10V_ASCII>,(R10)
BRB OPEN
BICB #<10V_ASCII>,(R10)
BSBB ENDFIELD
BBS #V F1,(R10),5$
MOVL QUAN-B(R11),CURDOT-B(R11)
BRB 10$
MOVL F1-B(R11),CURDOT-B(R11)
EXTZV #V PRMODE,#1,(R10),R0
INSV R0,#V PRG,#1,(R10)
BSBW LOCOUT
BBC #V F2,(R10),RSET
CMPL F2-B(R11),CURDOT-B(R11)
BLEQ RSET
IF NDF,SW,PROCESS
BSBB NEXTLOC
IFB
BSBW NEXTLOC
ENDC
BRB 15$
BRW ERROR

: DISPLAY IN ASCII
: SET ASCII FLAG
:
: CLEAR ASCII DISPLAY MODE
: TERMINATE FIELD
: ADDR SPECIFIED?
: : NO, GO INDIRECT
: AND DISPLAY CONTENT
: SET NEW DOT
: GET PROCESSOR REGISTER MODE FLAG
: AND MOVE TO SEMI-PERMANENT COPY
: OUTPUT AND OPEN
: RANGE SPECIFIED?
: CHECK FOR END
: YES
: INCREMENT TO NEXT DOT
: INCREMENT TO NEXT DOT
:
: AND CONTINUE
: DECLARE ERROR
```


		0284	460	.SBTTL	RETURN - CLOSE CURRENT OPEN CELL	
		0284	461			
		0284	462	:		
		0284	463	:	RETURN - CLOSE CURRENT OPEN CELL	
		0284	464	:		
		0284	465			
		0284	466	RETURN:		
18	10	0284	467	BSBB	ENDFIELD	:
		0286	468	.ENABL	LSB	:
0A 6A	00	0286	469	BBCC	#V_OPEN, (R10), 10\$:
03 6A	08	028A	470	BBC	#V_F1, (R10), RSET	:
	0560	028E	471	BSBW	DEPOSIT	:
	01E3	0291	472	RSET:	RESET	:
		0294	473	10\$:	#V_F1, (R10), RSET	:
F9 6A	08	0298	474	BRW	EQC1	:
	01D4	029B	475	.DSABL	LSB	:
					TERMINATE CURRENT FIELD	
					SKIP IF NONE OPEN	
					SKIP IF NOTHING TO STORE	
					DEPOSIT	
					RESET SCANNER	
					DONE IF NO INPUT	
					OTHERWISE OUTPUT	

		0298	477	.SBTTL	ENDFIELD - TERMINATE CURRENT FIELD	
		0298	478			
		0298	479			
		0298	480	:		
		0298	481	:	COMMA TERMINATE CURRENT FIELD	
FF68	30	0298	482	:		
		029E	483	:	COMMA: BSBW INFLD	; ZERO IF NULL FIELD
		029E	484	:		
		029E	485	:	TERMINATE CURRENT FIELD	
		029E	486	:		
		029E	487	:	ENDFIELD:	
16 6A	02	E5	029E	488	BBCC	#V, INFIELD, (R10), 10\$
	FF7A	30	02A2	489	BSBW	ENDEXPR
50	FC AB	9A	02A5	490	MOVZBL	FCTR-B(R11), R0
D3 01 AA	50	E2	02A9	491	BBSS	R0, 1(R10), ERR4
EO AB40	57	D0	02AE	492	MOVL	R7, F1-B(R11)[R0]
	FC AB	96	02B3	493	INCB	FCTR-B(R11)
	56	7C	02B6	494	CLRQ	R6
		05	02B8	495	RSB	
			02B9	496		

: CLEAR PENDING FIELD
: END EXPRESSION
: GET FIELD POINTER
: ERROR IF TOO MANY FIELDS
: STORE FIELD VALUE
: INCREMENT FIELD COUNTER
: CLEAR ACCUMULATORS
: RETURN

```
02B9 498 .SBTTL  FETCH - OBTAIN DATA SPECIFIED
02B9 499
02B9 500 :
02B9 501 :
02B9 502 :
02B9 503 :
02B9 504 :
02B9 505 :
02B9 506 :
02B9 507 :
02B9 508 :
02B9 509 :
02B9 510 :
02B9 511 :
02B9 512 :
02B9 513 :
02B9 514 :
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02B9 525 :
02B9 526 :
02B9 527 :
02B9 528 :
02B9 529 :
02B9 530 :
02B9 531 :
02B9 532 :
02B9 533 :
02B9 534 :
02B9 535 :
02B9 536 :
02B9 537 :
02B9 538 :

1D 6A 1F E0 02B9 504 BBS #V_PREG,(R10),40$ BR IF PROCESSOR REGISTER
02BD 505 .IF DF,SW_PROCESS :
02BD 506 TSTL PID-B(R11) CHECK FOR PROCESS GET
02BD 507 BNEQ 50$ BR IF YES
02BD 508 .ENDC
02BD 509 CASE CURTYPE-B(R11),TYPE=B,<- : OPERATE ON TYPE
02BD 510 10$,- BYTE
02BD 511 20$,- WORD
02BD 512 30$,- LONG
02BD 513 >
02BD 514 CASEB CURTYPE-B(R11),#0,S^#<<30005$-30004$>/2>-1
02BD 515 30004$: .SIGNED_WORD 10$-30004$
02BD 516 .SIGNED_WORD 20$-30004$
02BD 517 .SIGNED_WORD 30$-30004$
02BD 518 30005$:
02BD 519 10$: MOVZBL @CURDOT-B(R11),QUAN-B(R11) ; GET BYTE
02BD 520 RSB RETURN ;
02BD 521 20$: MOVZWL @CURDOT-B(R11),QUAN-B(R11) ; GET WORD
02BD 522 RSB RETURN ;
02BD 523 30$: MOVL @CURDOT-B(R11),QUAN-B(R11) ; GET LONGWORD
02BD 524 RSB RETURN ;
02BD 525 .IF NDF,SW_PROCESS :
02BD 526 MFPR CURDOT-B(R11),QUAN-B(R11) ; GET PROCESSOR REGISTER
02BD 527 MFPR CURDOT-B(R11),QUAN-B(R11) ;
02BD 528 RSB
02BD 529 .IFF : FALSE IF PROCESS VERSION
02BD 530 40$: $CMKRNLS B^FTCHPREG,(AF) : CALL IN KERNEL MODE TO FETCH
02BD 531 RSB
02BD 532 50$: BRW FETCHP : FETCH FROM FOREIGN PROCESS
02BD 533 .ENDC
02BD 534 .IF DF,SW_PROCESS :
02BD 535 FTCHPREG: .WORD 0 : ENTRY MASK
02BD 536 MOVAB W^PREXC,(FP) : SET EXCEPTION HANDLER
02BD 537 MFPR CURDOT-B(R11),QUAN-B(R11) ; GET PROCESSOR REGISTER
02BD 538 MOVL #1,R0 : RETURN SUCCESS
02BD 539 RET
02BD 540 .ENDC
```


- EXECUTIVE DEBUGGER
NEXTDOT - INCREMENT CURRENT LOCATION

```
16-SEP-1984 02:02:16 VAX/VMS Macro V04-00
5-SEP-1984 02:07:42 [MP.SRC]XDELTA.MAR;1
```

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(1)XD
VQ

				02DF	540	.SBTTL	NEXTDOT - INCREMENT CURRENT LOCATION	
				02DF	541			
				02DF	542	:		
				02DF	543	:	INCREMENT TO NEXT LOCATION	
				02DF	544	:		
				02DF	545	NEXTDOT:		
	51	01	D0	02DF	546	MOVL	#1,R1	: ASSUME UNIT INCREMENT
		6A	D5	02E2	547	TSTL	(R10)	: CHECK FOR PREG
		05	19	02E4	548	BLSS	10\$: YES, USE UNIT INCREMENT
51	51	FE	AB	02E6	549	ROTL	CURTYPE-B(R11),R1,R1	: FORM INCREMENT
	6B	51	C0	02EB	550	ADDL	R1,CURDOT-B(R11)	: AND ADD TO DOT
			05	02EE	551	RSB		: RETURN
				02EF	552			

```
02EF 554 .SBTTL OUTPUT - DISPLAY CONTENT
02EF 555 :
02EF 556 : OUTPUT CONTENT
02EF 557 :
02EF 558 OUTBB:
1C 0C 04 02EF 559 .BYTE 4,12,28 : STARTING DIGIT LIST
02F2 560 .SBTTL LINE FEED - DISPLAY NEXT
02F2 561 :
02F2 562 :
02F2 563 :
02F2 564 LINEFEED:
FF8F 30 02F2 565 BSBW RETURN : CLOSE OPEN CELL
02F5 566 NEXTLOC: : PROMPT WITH NEXT LOCATION
E8 10 02F5 567 BSBB NEXTDOT : INCREMENT LOCATION
02F7 568 LOC PROMPT: : DISPLAY ADDR/CONTENT
2B 10 02F7 569 BSBB OUTPUTA : OUTPUT ADDRESS
BE 10 02F9 570 LOCOUT: BSBB FETCH : FETCH CONTENT
6A 01 C8 02FB 571 BLSL #<1@V_OPEN>,(R10) : INDICATE OPEN CELL
02FE 572 :
02FE 573 OUTPUT: :
51 FE AB 9A 02FE 574 MOVZBL CURTYPE-B(R11),R1 : GET TYPE
52 E9 AF41 9A 0302 575 MOVZBL OUTBB[R1],R2 : INIT DIGIT SELECTOR
53 04 AB D0 0307 576 MOVL QUAN-B(R11),R3 : GET QUANTITY TO DISPLAY
04 6A 01 E0 030B 577 BBS #V ASCII,(R10),10$ : CHECK FOR ASCII OUT
53 10 030F 578 BSBB OUTCOM : OUTPUT NUMBER IN HEX
0E 11 0311 579 BRB 20$ : AND EXIT THROUGH OUTSPACE
08 AB 53 D0 0313 580 10$: MOVL R3,OUTBUF-B(R11) : PUT STRING IN BUFFER
52 01 51 78 0317 581 ASHL R1,#1,R2 : GET COUNT
08 AB42 94 031B 582 CLRB OUTBUF-B(R11)[R2] : MARK END OF STRING
59 10 031F 583 BSBB OUTZBUF : OUTPUT ASCIIZ BUFFER
008B 31 0321 584 20$: BRW OUTSPACE : FOLLOW WITH SPACE
0324 585
```

				0324	587	.SBTTL	OUTPUTA - OUTPUT ADDRESS	
				0324	588	:	OUTPUT ADDRESS	
				0324	589	:		
				0324	590	:		
				0324	591	:	OUTPUTA:	OUTPUT ADDRESS
				0324	592		BSBW	CRLF
53	008D	30		0327	593		MOVAB	SAVREG-B(R11),R3
	18 AB	9E		0328	594		.IF	DF,SW_PROCESS
				0328	595		TSTL	PID-B(R11)
				0328	596		BNEQ	3\$
				0328	597		.ENDC	
53	6B	53	C3	0328	598		SUBL3	R3,CURDOT-B(R11),R3
		12	19	032F	599		BLSS	5\$
	53	04	C6	0331	600		DIVL	#4,R3
	OF	53	D1	0334	601		CMPL	R3,#15
		0A	14	0337	602		BGTR	5\$
50	52	8F	9A	0339	603		MOVZBL	#A'R',R0
		52	10	033D	604		BSBB	OUTCHAR
		52	D4	033F	605		CLRL	R2
		13	11	0341	606		BRB	10\$
				0343	607		.IF	DF,SW_PROCESS
				0343	608	3\$:	TSTL	(R10)-
				0343	609		BLSS	5\$
				0343	610		MOVL	#28,R2
				0343	611		MOVL	PID-B(R11),R3
				0343	612		BSBB	OUTCOM
				0343	613		MOVZBL	#A':',R0
				0343	614		BSBB	OUTCHAR
				0343	615		.ENDC	
	53	6B	D0	0343	616	5\$:	MOVL	CURDOT-B(R11),R3
	52	1C	D0	0346	617		MOVL	#28,R2
		6A	D5	0349	618		TSTL	(R10)
		09	18	034B	619		BGEQ	10\$
50	50	8F	9A	034D	620		MOVZBL	#A'P',R0
		3E	10	0351	621		BSBB	OUTCHAR
	52	04	D0	0353	622		MOVL	#4,R2
		0C	10	0356	623	10\$:	BSBB	OUTCOM
	50	2F	9A	0358	624		MOVZBL	#SLSH,R0
		34	11	035B	625		BRB	OUTCHAR
				035D	626	OUTDIGIT:		
		52	D4	035D	627		CLRL	R2
		03	11	035F	628		BRB	OUTCOM
				0361	629			
				0361	630	OUTLONG:		
	52	1C	D0	0361	631		MOVL	#28,R2
				0364	632	OUTCOM:		
	54	08 AB	9E	0364	633		MOVAB	OUTBUF-B(R11),R4
51	53	04	EF	0368	634	10\$:	EXTZV	R2,#4,R3,R1
84	FDF6	CF41	90	036D	635		MOVB	PRIMARY[R1],(R4)+
		52	C2	0373	636		SUBL	#4,R2
		F0	18	0376	637		BGEQ	10\$
		64	94	0378	638		CLRB	(R4)
	54	08 AB	9E	037A	639	OUTZBUF:	MOVAB	OUTBUF-B(R11),R4
				037E	640			
				037E	641	OUTZSTRING:		
	50	84	9A	037E	642		MOVZBL	(R4)+,R0
		04	13	0381	643		BEQL	10\$

OUTPUT ADDRESS
OUTPUT CR/LF
BASE OF REGISTER AREA
ONLY FOR PROCESS VERSION
CHECK FOR OTHER PROCESS ADDRESS
BR IF YES
COMPUTE OFFSET INTO REGISTER AREA
NOT GENERAL REGISTER
SCALE TO LONGWORD NUMBER
CHECK FOR MAX REG NUMBER
GTR, NOT A REGISTER
OUTPUT PREFIX
OF 'R'
AND SET FOR ONE DIGIT OF OUTPUT
FOR PROCESS VERSION ONLY
CHECK FOR PROCESSOR REGISTER
BR IF YES
SET FOR LONGWORD OUTPUT
GET PID OF TARGET
OUTPUT PID AS LONGWORD
SEPARATE WITH ':'
OUTPUT COLON
GET ADDRESS
ASSUME LONGWORD OUTPUT
CHECK FOR PROCESSOR REGISTER
NO, JUST A LONGWORD
PRECEDE WITH A 'P'
OUTPUT P
SET FIELD TO 2 DIGITS
COMMON OUTPUT
OUTPUT SLASH
RETURN THROUGH OUTCHAR
OUTPUT ONE DIGIT
ZAP DIGIT SELECTOR
AND MERGE WITH COMMON
OUTPUT LONGWORD
SET DIGIT SELECTOR
FORMAT IT
GET ADDRESS OF OUTPUT BUFFER
GET DIGIT
BUFFER IT
NEXT DIGIT
DO ALL REQUESTED
MARK END OF BUFFER
GET START OF BUFFER
OUTPUT ASCIZ STRING
GET A CHAR
BR IF DONE

	0C	10	0383	644	BSBB	OUTCHAR	: OUTPUT CHAR
	F7	11	0385	645	BRB	OUTZSTRING	: CONTINUE
		05	0387	646	RSB		: RETURN IF DONE
			0388	647			
			0388	648			
			0388	649	OUTBSLSH:		: OUTPUT BACK SLASH
50	5C	8F	9A	0388	MOVZBL	#BSLSH,RO	: SET CHARACTER CODE
		03	11	038C	BRB	OUTCHAR	: AND OUTPUT IT
50	5B	9A	038E	652	OUTR8: MOVZBL	R8,RO	: GET CHAR TO OUTPUT
			0391	653	OUTCHAR:		: OUTPUT CHAR IN R0
			0391	654	.IF	NDF,SW_PROCESS	
	5C	D5	0391	655	TSTL	AP	: CHECK FOR CONSOLE
	05	12	0393	656	BNEQ	10\$: NO, USE DEVICE DIRECTLY
			0395	657	MFP	#PRS_TXCS,R1	: GET CONSOLE TRANSMIT STATUS
51	22	DB	0395		MFP	#PRS_TXCS,R1	
	04	11	0398	658	BRB	20\$: MERGE WITH COMMON CODE
51	04	AC	B0	039A	MOVW	OUTCR(AP),R1	: GET STATUS
EF	51	07	E1	039E	BBC	#7,R1,OUTCHAR	: WAIT FOR READY
		5C	D5	03A2	TSTL	AP	: CHECK FOR CONSOLE
	04	12	03A4	662	BNEQ	30\$: YES
23	50	DA	03A6	663	MTPR	RO,#PRS_TXDB	: SEND CHARACTER TO CONSOLE
		05	03A9	664	RSB		: RETURN
06	AC	50	90	03AA	MOVW	RO,OUTB(AP)	: OUTPUT CHAR
			03AE	666	.IFF		: FALSE FOR PROCESS VERSION
			03AE	667	PUSHL	RO	: BUFFER CHARACTER ON STACK
			03AE	668	MOVL	SP,RO	: SAVE POINTER TO IT
			03AE	669	\$QIO,S	EFN=#30,-	
			03AE	670		CHAN=TTCHAN,-	
			03AE	671		FUNC=#IOS_WRITEVBLK,-	
			03AE	672		P1=(RO),-	
			03AE	673		P2=#1	
			03AE	674	POPR	#^M<RO>	: BUFFER ADDRESS
			03AE	675	.ENDC		: ONE CHARACTER
		05	03AE	676	RSB		: RESTORE CHARACTER
			03AF	677	OUTSPACE:		: AND RETURN
50	20	9A	03AF	678	MOVZBL	#32,RO	: SET CODE FOR SPACE
	DD	11	03B2	679	BRB	OUTCHAR	: AND SEND IT
50	0D	9A	03B4	680	MOVZBL	#CR,RO	: RETURN
	D8	10	03B7	681	BSBB	OUTCHAR	: SEND IT
50	0A	9A	03B9	682	MOVZBL	#LF,RO	: LINE FEED
	D3	11	03BC	683	BRB	OUTCHAR	: SEND IT
			03BE	684			
			03BE	685			

```
03BE 687 .SBTTL GETCHAR - GET INPUT CHARACTER ROUTINE
03BE 688
03BE 689
03BE 690 GETCHAR - GET INPUT CHARACTER
03BE 691
03BE 692 OUTPUT:
03BE 693 R8 - INPUT CHARACTER
03BE 694 R9 - BUFFER POINTER UPDATED (BUFFER IN ASCIIZ FORMAT)
03BE 695
03BE 696
03BE 697 GETCHAR:
58 89 9A 03BE 698 MOVZBL (R9)+,R8 ; GET NEXT CHARACTER
01 13 03C1 699 BEQL 10$ ; READ IF NONE AVAIL
05 03C3 700 RSB
59 AC AB 9E 03C4 701 10$: MOVAB INBUF-B(R11),R9 ; SET ADDRESS OF INPUT BUFFER
03C8 702 .IF NDF,SW_PROCESS
05 03C8 703 20$: TSTL AP ; CHECK FOR CONSOLE
05 13 03CA 704 BEQL 30$ ; YES
50 6C B0 03CC 705 MOVW RDCR(AP),R0 ; GET STATUS
03 11 03CF 706 BRB 40$ ; CHECK STATUS
03D1 707 30$: MFPR #PR$_RXCS,R0 ; GET CONSOLE STATUS
50 20 DB 03D1 708 40$: MFPR #PR$_RXCS,R0
F0 50 07 E1 03D4 708 40$: BBC #7,R0,20$ ; WAIT FOR READY
05 03D8 709 TSTL AP ; CHECK FOR CONSOLE
06 13 03DA 710 BEQL 50$ ; YES
58 02 AC 90 03DC 711 MOVB RDBUF(AP),R8 ; GET CHARACTER
03 11 03E0 712 BRB 60$ ; MERGE WITH COMMON
03E2 713 50$: MFPR #PR$_RXDB,R8 ; GET CONSOLE CHARACTER
58 21 DB 03E2 714 .IFF
03E5 715 15$: MOVAB TERMASKD,R1 ; FALSE IF PROCESS VERSION
03E5 716 $Q10W_S EFN=#31,- ; ADDRESS OF TERMINATOR MASK DESCR
03E5 717 CHAN=TTCHAN,- ; INPUT DEVICE CHANNEL
03E5 718 IOSB=TTIOSB,- ; IO STATUS BLOCK
03E5 719 FUNC=#<10$_READVBLK>,-
03E5 720 P1=(R9),-
03E5 721 P2=#80,-
03E5 722 P4=R1
03E5 723 MOVZWL TTIOSB+2,R0
03E5 724 MOVB TTIOSB+4,(R0)+[R9]
03E5 725 CLRB (R9)[R0]
03E5 726 MOVL R9,R2
03E5 727 20$: MOVZBL (R2)+,R8 ; GET A CHARACTER
03E5 728 BEQL 15$ ; EMPTY, READ SOME MORE
03E5 729 .ENDC
58 80 8F 8A 03E5 730 60$: BICB #^X80,R8 ; STRIP PARITY
7F 8F 58 91 03E9 731 CMPB R8,#RUBOUT ; CHECK FOR RUBOUT
15 12 03ED 732 BNEQ 90$ ; NO
03 6A 06 E2 03EF 733 BBSS #V_RUB,(R10),70$ ; SET START OF RUBOUT SEQUENCE
FF 92 30 03F3 734 BSBW OUTBSL$H ; OUTPUT BACK SLASH
58 79 9A 03F6 735 70$: MOVZBL -(R9),R8 ; GET RUBBED OUT CHAR
04 12 03F9 736 BNEQ 80$ ; SKIP INC
59 06 D6 03FB 737 INCL R9 ; POINT AT START OF BUFFER
C9 11 03FD 738 BRB 20$ ; AND GET ANOTHER
FF 8C 30 03FF 739 80$: BSBW OUTR8 ; OUTPUT RUBBED OUT CHAR
C4 11 0402 740 BRB 20$ ; AND GET ANOTHER
03 6A 06 E5 0404 741 90$: BBCC #V_RUB,(R10),100$ ; TERMINATE RUBOUT SEQUENCE
```

		FF7D	30	0408	742	BSBW	OUTBSLSH	:	OUTPUT BACK SLASH
03	58	06	E1	0408	743	BBC	#6,R8,110\$:	BR IF NOT ALPHA
	58	20	8A	040F	744	BICB	#32,R8	:	SET TO UPPER CASE
				0412	745			:	
				0412	746	.IF	NDF,SW_PROCESS	:	
		FF79	30	0412	747	BSBW	OUTR8	:	ECHO CHARACTER
				0415	748	.ENDC		:	
	89	58	90	0415	749	MOVB	R8,(R9)+	:	BUFFER NEW CHAR
FD63	CF	08	58	3A	0418	LOCC	R8,#TERM,TERM	:	CHECK FOR TERMINATOR
		A8	13	041E	751	BEQL	20\$:	NOT A TERMINATOR
	58	0D	91	0420	752	CMPB	#CR,R8	:	IS CHAR = RETURN
		03	12	0423	753	BNEQ	120\$:	NO
		FF8C	30	0425	754	BSBW	CRLF	:	YES, SEND CR/LF
		69	94	0428	755	CLRB	(R9)	:	MARK END OF BUFFER
59	AC	AB	9E	042A	756	MOVAB	INBUF-B(R11),R9	:	RESTORE BUFFER BASE
		FF8D	31	042E	757	BRW	GETCHAR	:	AND TRY AGAIN

```
0431 759 .SBTTL PLUS/MINUS OPERATORS
0431 760 ::
0431 761 :: PLUS/MINUS OPERATORS
0431 762 ::
0431 763 BLANK: :: SAME AS PLUS
0431 764 OPERATOR: ::
FF AB 50 FDEB 30 0431 765 BSBW ENDEXPR :: END EXPR
83 0434 766 SUBB3 #OPERBAS,R0,OPER-B(R11) :: SET OPERATOR
05 0439 767 RSB :: RETURN
043A 768 ::
043A 769 :: MONADIC MINUS - NEGATE
043A 770 ::
56 D5 043A 771 NEGATE: TSTL R6 :: TEST ACCUMULATOR
03 13 043C 772 BEQL 5$ :: EMPTY
FDDE 30 043E 773 BSBW ENDEXPR :: OTHERWISE PERFORM OPERATION
6A 80 8F 8C 0441 774 5$: XORB #<1@V_NEGATE>,(R10) :: TOGGLE NEGATE FLAG
05 0445 775 10$: RSB :: AND RETURN
0446 776
0446 777
```



```
0446 779
0446 780 :
0446 781 :
0446 782 :
0446 783 TAB:
044A 784
044F 785
0454 786
0456 787
0456 788 :
0456 789 :
0456 790 :
0456 791
0456 792 ESCAP:
0456 793
0459 794
045B 795
045D 796
0462 797 10$:
0465 798 LOCP:

.SBTTL TAB - INDIRECT DISPLAY
TAB
MOVL QUAN-B(R11),CURDOT-B(R11) : GO INDIRECT
EXTZV #V_PMODE,#1,(R10),R0 : GET PROCESSOR REGISTER MODE
INSV R0,#V_PREG,#1,(R10) : AND COPY TO SEMI-PERMANENT FLAG
BRB LOCP : AND DISPLAY IT

ESCAPE - DISPLAY PREVIOUS LOCATION

MOVL #1,R1 : ASSUME UNIT INCREMENT
TSTL (R10) : CHECK FOR PROCESSOR REGISTER
BLSS 10$ : YES, USE UNIT INCREMENT
ROTL CURTYPE-B(R11),R1,R1 : FORM INCREMENT
SUBL R1,CURDOT-B(R11) : AND SUBTRACT FROM DOT
BRW LOCPROMPT : PROMPT WITH CONTENT
```

			0468	800	.SBTTL	EQUALS - DISPLAY VALUE	:	
			0468	801	:		:	
			0468	802	:	EQUALS - VALUE DISPLAY	:	
			0468	803	:		:	
			0468	804	EQUALS:		:	
			0468	805	.	ENABL	LSB	:
			0468	806	BSBW	ENDFIELD	:	TERMINATE FIELD
			0468	807	BBC	#V F1,(R10),10\$:	IGNORE IF FIELD BLANK
			046F	808	EQL1:	MOVL	F1=B(R11),QUAN-B(R11)	SET QUANTITY
			0474	809	10\$:	BSBW	OUTPUT	OUTPUT IT
			0477	810	:	BRB	RESET	RESET SCANNER
			0477	811	:	.DSABL	LSB	:
			0477	812	:		:	:
			0477	813	:		:	:
			0477	814	:	RESET	:	:
			0477	815	:		:	:
			0477	816	:		:	:
			0477	817	RESET:	BICL	#^XOFFFB0,(R10)	: CLEAR FIELD AND NEGATE FLAGS
			047E	818		CLRB	FCTR-B(R11)	: CLEAR FIELD COUNTER
			0481	819		CLRQ	R6	: RESET ACCUMULATORS
			0483	820		RSB		: RETURN

```
0484 822 .SBTTL SEMI - SECONDARY COMMAND SET
0484 823 :
0484 824 : SEMI
0484 825 :
0484 826 :
0484 827 SECOND:
58 0484 828 .ASCII /X/ : X REGISTER SET/DISPLAY
50 0485 829 .ASCII /P/ : P - PROCEED
4D 0486 830 .ASCII /M/ : M - SET MODIFY FLAG
49 0487 831 .ASCII /I/ : I - PROGRAM COUNTER
47 0488 832 .ASCII /G/ : G - GO, START
45 0489 833 .ASCII /E/ : E - EXECUTE STRING
42 048A 834 .ASCII /B/ : B - SET/CLR BREAKPOINT
00000007 048B 835 NSEC=-SECOND : NUMBER OF SECONDARY COMMANDS
048B 836 :
6A 01 8A 048B 837 SEMI: :
FE0D 30 048E 838 BICB #<12V OPEN>,(R10) : CLEAR OPEN FLAG
FF2A 30 0491 839 BSBW ENDFIELD : TERMINATE FIELD
EB AF 07 58 3A 0491 840 BSBW GETCHAR : GET SECONDARY COMMAND CHAR
0494 841 LOCC R8,#NSEC,SECOND : LOCATE SECONDARY COMMAND
0499 842 10$: CASE R0,LIMIT=#1,<- : SWITCH ON TYPE
0499 843 BRKPOINT,- : SET BREAKPOINT
0499 844 EXECUTE,- : EXECUTE STRING
0499 845 GO,- : SEMI-G, GO
0499 846 PROGCTR,- : SEMI-I, INSTRUCTION CONTER
0499 847 MFYFLGS,- : SEMI-M, MODIFY FLAG
0499 848 PROCED,- : SEMI-P, PROCEED
0499 849 XSET,- : SET XREGISTER
0499 850 >
06' 01 50 AF 0499 CASEW R0,#1,S^#<<30011$-30010$>/2>-1
049D 30010$: :
003A' 049D .SIGNED_WORD BRKPOINT-30010$
037A' 049F .SIGNED_WORD EXECUTE-30010$
00D8' 04A1 .SIGNED_WORD GO-30010$
0105' 04A3 .SIGNED_WORD PROGCTR-30010$
00EC' 04A5 .SIGNED_WORD MFYFLGS-30010$
00E1' 04A7 .SIGNED_WORD PROCED-30010$
0133' 04A9 .SIGNED_WORD XSET-30010$
04AB 30011$: :
FCFO 31 04AB 851 ERR2: BRW ERROR : ERROR
```

```
04AE 853 .SBTTL LEFT BRACKET - MODE SELECTION
04AE 854 :
04AE 855 :
04AE 856 :
04AE 857 :
04AE 858 :
43 04AE 859 MODES: : MODE CHARACTER LIST
4C 04AF 860 .ASCII /C/ : CHARACTER
57 04B0 861 .ASCII /L/ : LONG, HEX
42 04B1 862 .ASCII /W/ : WORD, HEX
00000004 04B2 863 NMODES=.-MODES : BYTE, HEX
04B2 864 : NUMBER OF MODE CHARACTERS
04B2 865 :
04B2 866 LBRACKET: : MODE SELECTION
F4 AF 04 FF09 30 04B2 867 BSBW GETCHAR : GET MODE CHAR
58 3A 04B5 868 LOCC R8,#NMODES,MODES : CONVERT TO INDEX
EF 13 04BA 869 BEQL ERR2 : NOT FOUND, ERROR
09 50 02 E0 04BC 870 BBS #2,R0,10$ : CHECK FOR 'C'
FE AB 50 01 83 04C0 871 SUBB3 #1,R0,CURTYPE-B(R11) : SET MODE
6A 02 8A 04C5 872 BICB #<1@V_ASCII>,(R10) : CLEAR CHAR MODE
05 04C8 873 RSB : RETURN
6A 02 88 04C9 874 10$: BISB #<1@V_ASCII>,(R10) : SET CHARACTER MODE
05 04CC 875 RSB
```


6A	02	03	01	F0	04CD	877	.SBTTL	SINGLE STEP	
				E5	04CD	878	STEP		
	00	6A	0F	04	04CD	879			
					04CD	880			
					04CD	881	STEP:	INSV	#1,#V TBIT,#2,(R10)
					04D2	882		BBCC	#V_PMODE,(R10),20\$
					04D6	883	20\$:	RET	

: CLR V_ATBRK, SET V TBIT
: CLEAR-PROCESSOR REGISTER DISPLAY MODE
: AND RETURN

```
04D7 885 .SBTTL BRKPOINT - SET/CLEAR BREAKPOINTS
04D7 886 :
04D7 887 : BRKPOINT
04D7 888 :
04D7 889 BRKPOINT:
58 6A 08 E1 04D7 890 BBC #V_F1,(R10),SHOBRK : DISPLAY BREAKPOINTS
12 6A 09 E0 04DB 891 BBS #V_F2,(R10),20$ : YES, IT WAS SPECIFIED
52 01 D0 04DF 892 MOVL #1,R2 : INIT INDEX
FBD1 CF42 D5 04E2 893 10$: TSTL BRKADR[R2] : FIND FREE SLOT
13 13 13 04E7 894 BEQL 30$ : YES, GOT ONE
FFF3 52 01 08 F1 04E9 895 ACBL #NBRK,#1,R2,10$ : CHECK THEM ALL
52 E4 AB D0 04EF 896 BRB ERR2 : ERROR
52 08 D1 04F5 897 20$: MOVL F2-B(R11),R2 : GET BRKPOINT NUMBER
52 08 D1 04F7 898 BEQL 10$ : NULL FIELD, SCAN FOR SLOT
FBD1 CF42 D4 04FA 900 CMPL #NBRK,R2 : CHECK FOR LEGAL
FBD1 CF42 D4 04FC 901 30$: BLSS ERR2 : OUT OF RANGE
FBFA CF42 D4 0501 902 CLRL BRKDSP[R2] : CLEAR DISPLAY
50 E0 AB D0 0506 903 CLRL BRKCOM[R2] : CLEAR COMMAND ADDRESS
03 13 050A 904 MOVL F1-B(R11),R0 : GET BREAKPOINT ADDRESS
050C 905 BEQL 35$ : ALLOW CLEAR OF BREAKPOINT
050C 906 .IF DF,SW PROCESS :
050C 907 PUSHB #M<R0,R1,R2,R3,R4,R5,R6> : ; SAVE REGISTERS FOR PROTECTION CHAN
050C 908 MOVL R0,R5 : SET START ADDRESS
050C 909 MOVL R0,R6 : AND END ADDRESS
050C 910 BSBW SETWRT : SET PAGE WRITABLE
050C 911 MOVL (SP),R0 : RESTORE BPT ADDRESS
60 60 90 050C 912 .ENDC :
050F 913 MOVB (R0),(R0) : TEST WRITABILITY OF ADDRESS
050F 914 .IF DF,SW PROCESS :
050F 915 BSBW REPROT : RESTORE PROTECTION
050F 916 POPR #M<R0,R1,R2,R3,R4,R5,R6> : ; AND REGISTERS
050F 917 .ENDC :
0C 6A 0A E1 050F 918 35$: BBC #V_F3,(R10),40$ : DISPLAY SPECIFIED?
FBC6 CF42 E8 AB D0 0513 919 MOVL F3-B(R11),BRKDSP[R2] : SET DISPLAY START
03 13 051A 920 BEQL 40$ : SKIP TEST IF NULL
E8 BB D5 051C 921 TSTL @F3-B(R11) : CHECK READABILITY
07 6A 0B E1 051F 922 40$: BBC #V_F4,(R10),45$ : SKIP IF NO COMMAND ADDRESS
FBD6 CF42 EC AB D0 0523 923 MOVL F4-B(R11),BRKCOM[R2] : SET COMMAND STRING
FB88 CF42 50 D0 052A 924 45$: MOVL R0,BRKADR[R2] : SAVE BREAKPOINT ADDRESS
FF44 31 0530 925 BRW RESET : RESET SCANNER AND RETURN
0533 926 :
0533 927 : SHOBRK
0533 928 :
0533 929 SHOBRK:
58 55 01 D0 0533 930 10$: MOVL #1,R5 : INIT INDEX FOR LOOP
FB7D CF45 D0 0536 931 MOVL BRKADR[R5],R8 : GET BREAKPOINT ADDRESS
2E 13 053C 932 BEQL 20$ : SKIP IF NULL
53 55 D0 053E 933 MOVL R5,R3 : BREAKPOINT NUMBER
FE70 30 0541 934 BSBW CRLF : NEW LINE
FE16 30 0544 935 BSBW OUTDIGIT : BPT NUMBER
FF65 30 0547 936 BSBW OUTSPACE : SPACE
53 58 D0 054A 937 MOVL R8,R3 : ADDRESS OF BPT
FE11 30 054D 938 BSBW OUTLONG : OUTPUT ADDRESS
F5C 30 0550 939 BSBW OUTSPACE : SPACE OVER
53 FB88 CF45 D0 0553 940 MOVL BRKDSP[R5],R3 : GET DISPLAY START
03 13 0559 941 BEQL 15$ : NONE
FE03 30 055B 942 BSBW OUTLONG : OUTPUT DISPLAY START
```

53	FB9D	CF45	D0	055E	942	15\$:	MOVL	BRKCOM[R5],R3	:	GET COMMAND STRING ADDRESS
		06	13	0564	943		BEQL	20\$:	NONE
		FE46	30	0566	944		BSBW	OUTSPACE	:	SPACE ANOTHER
		FDF5	30	0569	945		BSBW	OUTLONG	:	AND OUTPUT A LONGWORD
FFC4 55	01	08	F1	056C	946	20\$:	ACBL	#NBRK,#1,R5,10\$:	DO THEM ALL
		FE3F	31	0572	947		BRW	CRLF	:	AND EXIT THROUGH CRLF

				0575	949			.SBTTL	GO - START EXECUTION AT SPECIFIED LOCATION	
				0575	950	:				
				0575	951	:		GO		
				0575	952	:				
05	6A	08	E1	0575	953	:	GO:	BBC	#V_F1,(R10),PROCD	: JUST PROCEED IF NO VALUE
54	AB	E0	AB	D0	0579	:		MOVL	F1=B(R11),SAVPC-B(R11)	: SET NEW PC
				057E	955	:		BRW	PROCD	: FALL INTO PROCEED
				057E	956	:				
				057E	957	:		PROCEED		
				057E	958	:				
				057E	959	:	PROCD:		:	
04				057E	960	:		RET	:	RETURN


```
057F 962 .SBTTL SEMI-I, PC VALUE
057F 963 :
057F 964 : SEMI-I
057F 965 :
F8 AB FC9D 30 057F 966 COLON: BSBW ENDEXPR : TERMINATE EXPRESSION
57 DO 0582 967 MOVL R7,PID-B(R11) : SET PID FOR PROCESS
56 7C 0586 968 CLRQ R6 : RESET ACCUMULATORS
05 0588 969 RSB :
0589 970 :
51 F4 AB DE 0589 971 MFYFLGS:MOVAL MFYFLG-B(R11),R1 : SET MODIFY FLAG ADDRESS
17 11 058D 972 BRB VALUE : SET/GET VALUE
51 6B DE 058F 973 DOT: MOVAL CURDOT-B(R11),R1 : SET ADDRESS OF DOT
18 6A 1F E1 0592 974 BBC #V_PREG,(R10),VALR : WAS IT PROCESSOR REGISTER?
14 6A 0F E2 0596 975 BBSS #V_PMODE,(R10),VALR : YES, SET PROCESSOR REGISTER MODE
12 11 059A 976 BRB VALR : READ VALUE
51 04 AB DE 059C 977 QUANT: MOVAL QUAN-B(R11),R1 : SET QUANTITY ADDRESS
0C 11 05A0 978 BRB VALR : READ VALUE
05A2 979 :
51 54 AB DE 05A2 980 MOVAL SAVPC-B(R11),R1 : SET PC ADDRESS
04 6A 08 E1 05A6 981 VALUE: BBC #V_F1,(R10),VALR : SKIP IF NO VALUE
61 ED AB DO 05AA 982 MOVL F1-B(R11),(R1) : SET NEW VALUE FOR PC
56 61 DO 05AE 983 VALR: MOVL (R1),R6 : AND GET VALUE
FC52 31 05B1 984 VALI: BRW INFLD : SET FIELD IN PROGRESS
05B4 985 :
55 18 AB DE 05B4 986 MOVAL SAVREG-B(R11),R5 : SET BASE OF REGISTER AREA
02 10 05B8 987 BSBB REGCOM : FETCH ADDRESS
F5 11 05BA 988 BRB VALI : AND USE IT
FBA3 CF 10 FDF5 30 05BC 989 REGCOM: BSBW GETCHAR : GET SECOND CHAR
58 3A 05BF 990 LOCC R8,#16,PRIMARY : TRANSLATE TO HEX
05C5 991 .IF DF_SW_PROCESS : FOR PROCESS VERSION
05C5 992 BNEQ 10$ : LEGAL HEX DIGIT
05C5 993 CMPW #*A/X1/,-2(R9) : CHECK FOR EXIT COMMAND
05C5 994 BNEQ ERR3 : NO, ERROR
05C5 995 $EXIT_S EXITCODE : YES EXIT
05C5 996 .IFF :
43 13 05C5 997 BEQL ERR3 : ERROR, NOT HEX
05C7 998 .ENDC :
05C7 999 10$: :
50 10 50 C3 05C7 1000 SUBL3 R0,#16,R0 : INVERT
56 6540 DE 05CB 1001 MOVAL (R5)[R0],R6 : ACCUMULATE
05 05CF 1002 RSB : RETURN
05D0 1003 :
51 E4 AB 36 6A 09 E1 05D0 1004 XSET: BBC #V_F2,(R10),ERR3 : ERROR IF NOT TWO FIELDS
51 51 FB45 CF41 00 EF 05D4 1005 EXTZV #0,#4,F2-B(R11),R1 : GET REGISTER NUMBER
C4 11 05DA 1006 MOVAL XREGV[R1],R1 : AND COMPUTE REGISTER ADDRESS
05E0 1007 BRB VALUE : PROCESS VALUE
55 FB3E CF DE 05E2 1008 XREG: : X-REGISTER VALUE
D3 10 05E7 1009 MOVAL XREGV,R5 : SET ADDRESS OF REGISTER VECTOR
56 66 DO 05E9 1010 BSBB REGCOM : ADDRESS TO R6
C3 11 05EC 1011 MOVL (R6),R6 : GET VALUE
05EE 1012 BRB VALI : AND NOTE INPUT IN FIELD
05F0 1013 .ALIGN LONG : LONGWORD ALIGN EXCEPTION ROUTINES
05F0 1014 XDELACV: : ACCESS VIOLATION HANDLER
05F0 1015 MCHK: : MACHINE CHECK
05F0 1016 .IF NDF_SW_PROCESS :
5C D5 05F0 1017 TSTL AP : CHECK FOR SIMULATOR
16 12 05F2 1018 BNEQ ERR3 : YES, SKIP RESET
```

```
02' 01 00000000'GF 8F 05F4 1019
                        05F4 1020
                        05F4 1021 CPUDISP <CLR_780,-
                        05F4 1022 CLR_750,-
                        05F4 1023 CLR_730> *DISPATCH ON CPU TYPE*
0006' 05FC 30012$: CASEB G^EXESGB_CPUYPE,#PRS_SID_TYP780,S^#<<30013$-30012$>/2>-1
000B' 05FC .SIGNED_WORD CLR_78C-30012$
000B' 05FE .SIGNED_WORD CLR_750-30012$
000B' 0600 .SIGNED_WORD CLR_730-30012$
0602 30013$:
0602 1024
0602 1025 CLR_780:
0602 1026 MTPR #0,#PRS_SBIFS
0605 1027 BRB CLR_END
0607 1028
0607 1029 CLR_730:
0607 1030 CLR_750:
0607 1031 MTPR #^XF,#PRS_MCESR
060A 1032
060A 1033 CLR_END:
060A 1034
060A 1035
060A 1036 .ENDC
060A 1037 10$:
FB91 31 060A 1038 ERR3: BRW ERROR
060D 1039
```

FOR 11/780:
CLEAR SBI FAULT
ERROR CLEARED

FOR 11/730:
FOR 11/750:
SET 1 TO CLEAR MCHECK ERROR SUMMARY

END OF CPU-DEPENDENT CODE

AND DECLARE ERROR

```
060D 1041 .SBTTL REGISTER SAVE AND RESTORE
060D 1042
060D 1043 :
060D 1044 :
060D 1045 :
060D 1046 :
060D 1047 :
060D 1048 :
12 1F DA 060D 1049 :
FASB CF 50 7D 0610 1050 :
51 FASF CF 9E 0615 1051 :
061A 1052 :
061A 1053 :
061A 1054 :
061A 1055 :
061A 1056 :
061A 1057 :
061A 1058 :
061A 1059 :
061A 1060 :
061A 1061 :
81 52 7D 061A 1062 :
81 54 7D 061D 1063 :
81 56 7D 0620 1064 :
81 58 7D 0623 1065 :
81 5A 7D 0626 1066 :
0629 1067 :
81 5C 7D 0629 1068 :
81 0C AE 9E 062C 1069 :
81 04 AE 7D 0630 1070 :
0634 1071 :
0634 1072 :
0634 1073 :
0634 1074 :
0634 1075 :
0634 1076 :
0634 1077 :
0634 1078 :
0634 1079 :
81 22 DB 0634 1080 :
81 20 DB 0637 1081 :
5C D4 063A 1082 :
063C 1083 :
22 00 DA 063C 1084 :
20 00 DA 063F 1085 :
0642 1086 :
0642 1087 :
5B FA12 CF 9E 0642 1088 20$:
0647 1089 :
0647 1090 :
0647 1091 :
0647 1092 :
5A DC AB 9E 0647 1093 :
59 AC AB 9E 064B 1094 :

SAVE:
IF NDF,SW_PROCESS
SETIPL #31
MTPR #31,S^#PRS_IPL
JSB INISWRITABLE
MOVQ R0,SAVREG
MOVAB SAVR2,R1
IF
$SETAST,S #0
PUSHAB -(R0)
MOVPSL R1
EXTZV #PSL$V_CURMOD,#PSL$S_CURMOD,R1,R1
MULW #CONTEXT$2,R1
MOVAB SAVREG[R1],R1
MOVL 8(AP),R0
MOVQ 12(R0),(R1)+
.ENDC
MOVQ R2,(R1)+
MOVQ R4,(R1)+
MOVQ R6,(R1)+
MOVQ R8,(R1)+
MOVQ R10,(R1)+
IF NDF,SW_PROCESS
MOVQ AP,(R1)+
MOVAB 12(SP),(R1)+
MOVQ 4(SP),(R1)+
IF
MOVQ 8(FP),(R1)+
SUBL3 #1,24(AP),R0
MOVAL 24(AP)[R0],R0
MOVAL 8(R0),(R1)+
MOVQ (R0),(R1)+
.ENDC
IF NDF,SW_PROCESS
MFPR #PRS_TXCS,(R1)+
MFPR #PRS_TXCS,(R1)+
MFPR #PRS_RXCS,(R1)+
MFPR #PRS_RXCS,(R1)+
CLRL AP
.ENDC
IF NDF,SW_PROCESS
MTPR #0,#PRS_TXCS
MTPR #0,#PRS_RXCS
.ENDC
IF NDF,SW_PROCESS
MOVAB B,R11
IF
W^<B-<SAVPSL+4>>(R1),R11
MOVL (SP)+,ASTEN-B(R11)
.ENDC
MOVAB STATUS-B(R11),R10
MOVAB INBUF-B(R11),R9

DISABLE
MAKE THE SYSTEM WRITABLE
SAVE R0,R1
SETUP BASE FOR REMAINING REGS
FALSE IF PROCESS VERSION
DISABLE ASTS
SAVE ENABLE VALUE-1
GET CURRENT PSL
ISOLATE CURRENT MODE
COMPUTE OFFSET TO PROPER CONTEXT AREA
FORM ADDRESS OF REGISTER SAVE
GET POINTER TO MECHANISM
SAVE R0,R1
SAVE R2,R3
SAVE R4,R5
SAVE R6,R7
SAVE R8,R9
SAVE R10,R11
SAVE AP,FP
ASSUME KERNEL STACK
SAVE PC,PSL
SAVE AP,FP
GET NUMBER OF ARGS IN SIGNAL
POINT TO PC,PSL
COMPUTE SP
SAVE PC,PSL
SAVE CONSOLE TRANSMIT STATUS
SAVE CONSOLE RECVR STATUS
ZAP DEVICE ADDRESS BASE
CLEAR INTERRUPT ENABLE
FOR BOTH TRANSMIT AND RECEIVE
AND DATA BASE ADDRESS
FALSE FOR PROCESS VERSION
SET BASE OF CONTEXT AREA
SAVE AST ENABLE
SET STATUS BASE
POINT TO INPUT BUFFER
```

```

        69  94 064F 1095 CLR B (R9) ; MAKE BUFFER EMPTY
        0084 30 0651 1096 .IF NDF,SW_PROCESS ;
        FBOA CF 04 AO DO 0651 1097 BSBW GET SCB ; GET BASE OF SCB
        04 AO 93 AF 9E 0654 1098 MOVL 4(R0),MCHKSAV ; SAVE ORIGINAL MCHK VECTOR
        20 AO BE AF 9E 065A 1099 MOVAB MCHK,4(R0) ; SET TO XDELTA VECTOR
        24 AO 89 AF 9E 065F 1100 MOVAB XDELACV,*X20(R0) ; SET ACCESS VIOLATION VECTOR
        18 AO 84 AF 9E 0664 1101 MOVAB XDELACV,*X24(R0) ; SET PG FAULT VECTOR
50 08 AE 02 18 EF 0669 1102 MOVAB XDELACV,*X18(R0) ; SET RESERVED OPERAND HANDLER
        07 13 066E 1103 EXTZV #PSL$V_CURMOD,#PSL$S_CURMOD,8(SP),R0 ; GET MODE
        50 00 CO 0674 1104 BEQL 30$ ; CORRECT ALREADY IF KERNEL
        50 AB 50 DB 0676 1105 ADDL #PRS_KSP,R0 ; COMPUTE PROCESSOR REGISTER
        FDF7 31 0679 1106 MFPR R0,SAVSP-B(R11) ; AND SAVE CORRECT SP
        067D 1107 .ENDC
        067D 1108 30$: BRW RESET ; RESET SCANNER
        0680 1109
        0680 1110
        0680 1111
        0680 1112
        0680 1113 RESTORE: ; RESTORE TARGET REGISTERS
        0680 1114 .IF NDF,SW_PROCESS ; RESTORE EVERYTHING
        04 AE 54 AB 7D 0680 1115 MOVQ SAVPC-B(R11),4(SP) ; SET PC,PSL
        0685 1116 .IFF FALSE IF PROCESS
        0685 1117 SUBL3 #1,24(AP),R0 ; GET SIGNAL ARG COUNT
        0685 1118 MOVAL 24(AP),R0 ; COMPUTE ADDRESS OF PC,PSL
        0685 1119 MOVQ SAVPC-B(R11),(R0) ; STORE UPDATED PC,PSL
        0685 1120 .ENDC
        0685 1121 RESTORR: ; RESTORE REGISTERS ONLY
        0685 1122 .IF NDF,SW_PROCESS
        20 AO 00000000'EF 51 10 0685 1123 BSBW GET SCB ; GET BASE OF SCB
        24 AO 00000000'EF 9E 9E 0687 1124 MOVAB EXESACVIOLAT,*X20(R0) ; RESTORE ACCESS VECTOR
        04 AO FAC9 CF DO 068F 1125 MOVAB MMG$PAGEFAULT,*X24(R0) ; AND PAGE FAULT VECTOR
        18 AO 00000000'EF 9E 9E 0697 1126 MOVL MCHKSAV,4(R0) ; RESTORE MACHINE CHECK VECTOR
        5C 0A 12 06A5 1127 MOVAB EXESROPAND,*X18(R0) ; RESTORE RESERVED OPERAND VECTOR
        22 5C AB DA 06A7 1128 TSTW AP ; CHECK FOR CONSOLE
        20 60 AB DA 06A9 1129 BNEQ 10$ ; NO, OTHER DEVICE
        04 AC 5C AB B0 06AD 1130 MTPR SAVOCR-B(R11),#PRS_TXCS ; RESTORE INITIAL TX STATUS
        6C 5E AB B0 06AD 1131 MTPR SAVRXCS-B(R11),#PRS_RXCS ; AND INITIAL RECEIVER STATE
        51 20 AB 9E 06B1 1132 BRB 20$ ; MERGE WITH COMMON CODE
        52 81 7D 06B3 1133 10$: MOVW SAVOCR-B(R11),OUTCR(AP) ; RESTORE OUTPUT CSR
        54 81 7D 06B8 1134 MOVW SAVRCR-B(R11),RDCR(AP) ; AND INPUT CSR CONTENT
        56 81 7D 06BC 1135 .IFF
        58 81 7D 06BC 1136 PUSHL ASTEN-B(R11) ; SAVE AST ENABLE
        5A 81 7D 06BC 1137 .ENDC
        50 5C 81 7D 06BC 1138 20$: MOVAB SAVR2-B(R11),R1 ; SET BASE FOR RESTORE
        F99A CF 7D 06C0 1139 MOVQ (R1)+,R2 ; RESTORE R2,R3
        52 81 7D 06C3 1140 MOVQ (R1)+,R4 ; RESTORE R4,R5
        54 81 7D 06C6 1141 MOVQ (R1)+,R6 ; RESTORE R6,R7
        56 81 7D 06C9 1142 MOVQ (R1)+,R8 ; RESTORE R8,R9
        58 81 7D 06CC 1143 MOVQ (R1)+,R10 ; RESTORE R10,R11
        5A 81 7D 06CF 1144 .IF NDF,SW_PROCESS
        50 5C 81 7D 06CF 1145 MOVQ (R1)+,AP ; RESTORE AP,FP
        52 81 7D 06D2 1146 MOVQ SAVREG,R0 ; RESTORE R0,R1
        54 81 7D 06D7 1147 .IFF
        56 81 7D 06D7 1148 MOVQ (R1)+,8(FP) ; RESTORE AP,FP
        58 81 7D 06D7 1149 MOVL 8(AP),R0 ; SET NEW VALUES FOR AP,FP
        5A 81 7D 06D7 1150 MOVQ <SAVREG-SAVSP>(R1),12(R0) ; GET MECHANISM POINTER
        ; STORE UPDATED R0,R1
```



```
06D7 1151      MOVPSL R1      : GET CURRENT PSL
06D7 1152      EXTZV  #PSL$V_CURMOD,#PSL$S_CURMOD,R1,R1 : GET CURRENT MODE
06D7 1153      BBCC   R1,DBGACTIVE,30$ : CLEAR ACTIVE BIT FOR MODE
06D7 1154 30$:  TSTL   (SP)+    : CHECK FOR AST ENABLE
06D7 1155      BEQL   35$      : NO
06D7 1156      SSETAST_5      #1 : RE- ENABLE AST RECOGNITION
06D7 1157      :              :
06D7 1158 35$:  .ENDC          :
06D7 1159      .IF   NDF,SW_PROCESS :
06D7 1160      JSB   INISRONLY      :
06D7 1161      .ENDC          :
DS 06D7 1162      RSB              :
06D7 1163      :              : AND RETURN
```

```
06D8 1166 .SBTTL GET SCB ADDRESS
06D8 1167
06D8 1168
06D8 1169 : SUBROUTINE GETSCB IS CALLED TO GET THE PHYSICAL OR VIRTUAL
06D8 1170 : ADDRESS OF THE CURRENT SCB.
06D8 1171 :
06D8 1172 : INPUTS: NONE
06D8 1173 :
06D8 1174 : OUTPUTS: R0 = SCB ADDRESS
06D8 1175 : OTHER REGISTERS PRESERVED
06D8 1176 :
06D8 1177
06D8 1178 .IF NDF,SW PROCESS ; NOT FOR PROCESS VERSION
06D8 1179 GETSCB: MFPR #PRS_MAPEN,R0 ; GET MAPPING STATUS
06D8 1180 BNEQ 10$ ; BRANCH IF MAPPING ENABLED
06DD 1181 MFPR #PRS_SCBB,R0 ; ELSE GET PHY ADDR OF SCB
06DD 1182 MFPR #PRS_SCBB,R0
06E0 1183 BRB 20$ ; JOIN COMMON RETURN
06E2 1184 10$: MOVAL SCBSAL_BASE,R0 ; IF MAPPING ENABLED, GET SCB VA
06E9 1185 20$: RSB ; RETURN
06EA 1185 .ENDC ;
```

50 38 DB 06D8 1166
05 12 06D8 1167
50 11 DB 06D8 1168
07 11 06D8 1169
50 00000000'EF DE 06E0 1182
05 06E2 1183
06E9 1184
06EA 1185

HEX	ASCII	DISASM	COMMENT
00 20 54 41 20 4B 52 42 20		06EA 1187 .SBTTL BPT TRAP HANDLER	
		06EA 1188 :	
		06EA 1189 : HANDLE BREAKPOINT TRAPS	
		06EA 1190 :	
		06EA 1191 BMSG: .ASCIZ / BRK AT /	BREAK POINT MESSAGE
		06F3 1192 .ALIGN LONG	LONGWORD ALIGNMENT
		06F4 1193 .IF NDF,SW_PROCESS	EXEC VERSION
		06F4 1194 XDELBPT: :	XDELTA BPT ENTRY
		06F4 1195 .IFF	
		06F4 1196 XDELBPT: :	DELTA BPT ENTRY
		06F4 1197 .ENDC	
FF16 30		06F4 1198 BSBW SAVE	SAVE REGS AND DISABLE
00D3 30		06F7 1199 BSBW GETBPTX	GET INDEX OF BPT
53 D5		06FA 1200 TSTL R3	CHECK FOR MATCH
10 12		06FC 1201 BNEQ 10\$	YES, FOUND IT
FF84 30		06FE 1202 BSBW RESTORR	RESTORE REGISTERS ONLY
		0701 1203 .IF NDF,SW_PROCESS	
7E 06 AE 9A		0701 1204 MOVZBL 6(SP),-(SP)	GET IPL
		0705 1205 ENBINT	ENABLE
12 8E DA		0705 1205 MTPR (SP)+,S*#PR\$-IPL	
00000000'EF 17		0708 1206 JMP EXESBREAK	AND HANDLE NORMALLY
		070E 1207 .IFF	FALSE IF PROCESS VERSION
		070E 1208 :	
		070E 1209 : ***** UNEXPECTED BREAKPOINT *****	
		070E 1210 CLRL R0	RETURN FALSE
		070E 1211 RET	
		070E 1212 .ENDC	
6A 18 88		070E 1213 10\$: BISB #<<12V_TBIT>!<12V_ATBRK>>,(R10) ; SET STATUS	
		0711 1214 30\$:	
		0711 1215 BSBW UNBRK	RESTORE OPCODES
38 58 AB 0081 30		0714 1216 BBS #PSL\$V_TBIT,SAVPSL-B(R11)	PROCEED ; PROCEED IF BPT AND TBIT
55 53 D0		0719 1217 MOVL R3,R5	SAVE BPT NUMBER
		071C 1218 BSBW CRLF	OUTPUT CR/LF PAIR
		071F 1219 BSBW OUTDIGIT	OUTPUT BPT NUMBER
54 C5 AF 9E		0722 1220 MOVAB BMSG,R4	MSG ADDRESS
		0726 1221 BSBW OUTZSTRING	OUTPUT ASCIIZ
53 54 AB D0		0729 1222 MOVL SAVPC-B(R11),R3	OUTPUT PC
		072D 1223 BSBW OUTLONG	OUTPUT HEX LONGWORD
		0730 1224 BSBW OUTSPACE	SEND SPACE
51 F9A8 CF45 D0		0733 1225 MOVL BRKDSP[R5],R1	GET ADDRESS TO DISPLAY
		0739 1226 BEQL 40\$	NONE
6B 51 D0		073B 1227 MOVL R1,CURDOT-B(R11)	SET AS CURRENT DOT
		073E 1228 BSBW LOC PROMPT	AND DISPLAY
51 F9BA CF45 D0		0741 1229 40\$: MOVL BRKCOM[R5],R1	GET COMMAND STRING ADDRESS
		0747 1230 BEQL	NONE GET COMMAND
59 51 D0		0749 1231 MOVL R1,R9	SET TO SCAN STORED COMMAND
		074C 1232 GETCMD: :	GET COMMANDS
		074C 1233	
FA49 CF 6C FA		074C 1234 CALLG (AP),DCOM	PERFORM DEBUG COMMANDS
		0751 1235 PROCEED: :	PROCEED
		0751 1236 BSBB SETBRK	SET BREAKPOINTS
09 6A 03 E5		0753 1237 BBCC #V TBIT,(R10),50\$	TEST AND CLR TRACE FLAG
00 58 AB 04 E2		0757 1238 30\$: BBSS #PSL\$V_TBIT,SAVPSL-B(R11)	40\$; SET TBIT
		075C 1239 40\$:	
		075C 1240 .IF DF,SW_PROCESS	FOR PROCESS VERSION
		075C 1241 CMPB #2,SAVPC-B(R11)	CHECK FOR REI OP CODE
		075C 1242 BNEQ 45\$	NO, NOTHING SPECIAL

00 6A	05	E2	075C	1243	EXTZV	#PSL\$V_CURMOD,#PSL\$S_CURMOD,SAVPSL-B(R11),R0	: GET NEW MODE
			075C	1244	MULW	#CONTEXT\$Z,R0	: SCALE BY PER MODE CONTEXT AREA SIZE
			075C	1245	MOVAB	STATUS-B(R0),R10	: POINT TO NEW FLAGS
			075C	1246	.ENDC		
			075C	1247	45\$:	BBSS	#V_TBITOK,(R10),50\$
			0760	1248	50\$:	BSBW	RESTORE
			0763	1249	.IF	NDF,\$W_PROCESS	
		02	0763	1250	REI		: AND RETURN
			0764	1251	.IFF		: FALSE IF PROCESS VERSION
			0764	1252	MOVL	#1,R0	: RETURN TRUE
			0764	1253	RET		
			0764	1254	.ENDC		
			0764	1255			


```
0764 1257 .SBTTL TBIT EXCEPTION HANDLER
0764 1258 :
0764 1259 : HANDLER FOR TBIT EXCEPTION
0764 1260 :
0764 1261 .ALIGN LONG : LONGWORD ALIGNED
0764 1262 .IF NDF,SW_PROCESS :
0764 1263 XDELTBIT: : XDELTA TBIT HANDLER
0764 1264 .IFF :
0764 1265 XDELTBIT: :
0764 1266 .ENDC :
10 6A FE A6 30 0764 1267 BSBW SAVE : SAVE AND DISABLE
05 E4 0767 1268 BBSC #V_TBITOK,(R10),XDELDBG : BR IF TBIT EXPECTED
FF 17 30 0768 1269 BSBW RESTORR : RESTORE REGISTERS
7E 06 AE 9A 076E 1270 .IF NDF,SW_PROCESS :
076E 1271 MOVZBL 6(SP),=(SP) : GET IPL FOR ENABLE
12 8E DA 0772 1272 ENBINT : ENABLE
00000000'EF 17 0772 1273 JMP MTPR (SP)+,S^#PRS_IPL : OTHERWISE LET EXEC HANDLE
077B 1274 .IFF EXESTBIT : FALSE IF PROCESS VERSION
077B 1275 CLRL R0 : RESIGNAL
077B 1276 RET : UNEXPECTED TBIT EXCEPTION
077B 1277 .ENDC :
58 AB 10 CA 077B 1278 XDELDBG: : COMMON WITH DEBUG EXCEPTION
14 10 077B 1279 BICL #<1@PSL$V_TBIT>,SAVPSL-B(R11) : CLEAR TBIT IN PSL
CC 6A 04 E4 077F 1280 BSBW UNBRK : REPLACE OPCODES
0781 1281 BBSC #V_ATBRK,(R10),PROCEED : CHECK FOR PROCEED
0785 1282 :
0785 1283 : OUTPUT STEP MESSAGE
0785 1284 :
6B 54 AB D0 0785 1285 MOVL SAVPC-B(R11),CURDOT-B(R11) : SET ADDRESS
00 BB 04 00 0C 0789 1286 IFNORD #4,@CURDOT-B(R11),GETCMD : SKIP DISPLAY IF NOT READABLE
BC 13 078E :
FB 64 30 0790 1287 BSBW BEQL GETCMD :
B7 11 0793 1288 BRB LOC PROMPT : PROMPT WITH ADDRESS/CONTENT
0795 1289 GETCMD : GO GET COMMANDS
```

```
0795 1291 .SBTTL UNBRK - RESTORE OPCODES FOR BREAKPOINTS
0795 1292 :
0795 1293 : UNBRK
0795 1294 :
0795 1295 UNBRK:
50 51 08 D0 0795 1296 MOVL #NBRK,R1 : INIT LOOP
F91B CF41 D0 0798 1297 10$: MOVL BRKADR[R1],R0 : GET BREAKPOINT ADDRESS
06 13 079E 1298 BEQL 20$ : SKIP IF NOT ENABLED
07A0 1299 .IF DF,SW,PROCESS :
07A0 1300 PUSHF #M<R0,R1,R2,R3,R4,R5> : SAVE REGS FOR PROTECTION CHANGE
07A0 1301 MOVL R0,R4 : FORM INADR RANGE FOR SET PROTECTION
07A0 1302 MOVL R0,R5 :
07A0 1303 BSBW SETWRT : SET PAGE WRITABLE
07A0 1304 MOVQ (SP),R0 : RESTORE R0,R1
60 F936 CF41 90 07A0 1305 .ENDC :
07A0 1306 MOVB BRKOP[R1],(R0) : RESTORE OPCODE
07A6 1307 .IF DF,SW,PROCESS :
07A6 1308 BSBW REPROT : RESTORE PROTECTION
07A6 1309 POPR #M<R0,R1,R2,R3,R4,R5> : RESTORE REGISTERS
EF 51 F5 07A6 1310 .ENDC :
05 07A6 1311 20$: SOBGTR R1,10$ : DO THEM ALL
07A9 1312 RSB : AND RETURN
07AA 1313
```

```
07AA 1315 .SBTTL SETBRK - SET BREAK POINT INSTRUCTIONS
07AA 1316 :
07AA 1317 :
07AA 1318 :
07AA 1319 SETBRK: MOVL #NBRK,R1 : INIT LOOP
50 51 08 D0 07AD 1320 10$: MOVL BRKADR[R1],R0 : GET ADDRESS
F906 CF41 D0 07B3 1321 : BEQL 20$ : SKIP IF NOT ENABLED
14 13 07B5 1322 : MOVBL (R0),BRKOP[R1] : SAVE OPCODE
F920 CF41 60 90 07BB 1323 : BITB #<<12V_TBIT>!!<12V_ATBRK>>, (R10) : CHECK FOR TRACE
6A 18 93 07BE 1324 : BEQL 15$ : NO TRACE, SET ANYWAY
06 13 07C0 1325 : CMPL R0,SAVPC-B(R11) : CHECK FOR AT BPT
54 AB 50 D1 07C4 1326 : BEQL 20$ : YES, DONT SET IT
03 13 07C6 1327 15$: :
07C6 1328 : IF DF,SW PROCESS :
07C6 1329 : PUSHB #*M<R0,R1,R2,R3,R4,R5> : SAVE REGISTERS FOR PROTECTION CHANGE
07C6 1330 : MOVL R0,R4 : SET START ADDRESS OF RANGE
07C6 1331 : MOVL R0,R5 : AND END ADDRESS
07C6 1332 : BSBW SETWRT : SET PAGE WRITABLE
07C6 1333 : MOVL (SP),R0 : RESTORE BPT ADDRESS
07C6 1334 : .ENDC :
60 03 90 07C6 1335 : MOVBL #3,(R0) : SET BREAKPOINT OPCODE
07C9 1336 : IF DF,SW PROCESS :
07C9 1337 : BSBW REPROT : RESTORE ORIGINAL PROTECTION VALUE
07C9 1338 : POPR #*M<R0,R1,R2,R3,R4,R5> : AND REGISTERS
07C9 1339 : .ENDC :
E1 51 F5 07C9 1340 20$: SOBGTR R1,10$ : DO THEM ALL
05 07CC 1341 : RSB : AND RETURN
07CD 1342
```

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			07CD	1344		.SBTTL	GETBPTX - GET INDEX FOR BREAKPOINT	
			07CD	1345	:			
			07CD	1346	:	GETBPTX		
			07CD	1347	:			
			07CD	1348	GETBPTX:			
	53	08	D0	07CD	1349	MOVL	#NBRK,R3	: INIT LOOP
F8E1 CF43	54	AB	D1	07D0	1350	10\$:	CMPL	SAVPC-B(R11),BRKADR[R3] : IS THIS A BPT?
		03	13	07D7	1351		BEQL	20\$: YES
	F4	53	F5	07D9	1352		SOBGTR	R3,10\$: NO, CONTINUE
			05	07DC	1353	20\$:	RSB	: RETURN

			07DD	1355		.SBTTL	QUOTE - INPUT CHARACTER STRING	
			07DD	1356	:			
			07DD	1357	:	QUOTE -	START CHARACTER STRING INPUT	
			07DD	1358	:			
			07DD	1359	:			
			07DD	1360	QUOTE:			
55	68	D0	07DD	1361	5\$:	MOVL	CURDOT-B(R11),R5	: POINT TO STRING BUFFER
	FBDB	30	07E0	1362		BSBW	GETCHAR	: GET CHARACTER
58	27	91	07E3	1363		CMPB	#QUOT,R8	: CHECK FOR QUOTE
	05	13	07E6	1364		BEQL	10\$: YES, END OF STRING
85	58	90	07E8	1365		MOVB	R8,(R5)+	: INSERT IN BUFFER
	F3	11	07EB	1366		BRB	5\$: AND CONTINUE
68	55	D0	07ED	1367	10\$:	MOVL	R5,CURDOT-B(R11)	: SAVE NEW DOT
		05	07F0	1367		RSB		: RETURN


```
07F1 1369 .SBTTL DEPOSIT
07F1 1370 :
07F1 1371 : DEPOSIT DATA
07F1 1372 :
07F1 1373 DEPOSIT:
1D 6A 1F EO 07F1 1374 BBS #V_PREG,(R10),40$ : BR IF PROCESSOR REGISTER
07F5 1375 .IF DF_SW_PROCESS : GET CURRENT DOT
07F5 1376 MOVL CURDOT-B(R11),R4 : CHECK FOR ARBITRARY PROCESS DEPOSIT
07F5 1377 TSTL PID-B(R11) : BR IF YES
07F5 1378 BNEQ 50$ :
07F5 1379 .ENDC :
07F5 1380 CASE CURTYPE-B(R11),TYPE=B,<- : SWITCH ON TYPE
07F5 1381 10$,- : BYTE
07F5 1382 20$,- : WORD
07F5 1383 30$,- : LONG
07F5 1384 >
02' 00 FE AB 8F 07F5 CASEB CURTYPE-B(R11),#0,S^N<<30020$-30019$>/2>-1
07FA 30019$:
0006' 07FA .SIGNED_WORD 10$-30019$
000C' 07FC .SIGNED_WORD 20$-30019$
0012' 07FE .SIGNED_WORD 30$-30019$
0800 30020$:
00 BB EO AB 90 0800 1385 .IF NDF_SW_PROCESS :
05 0805 1386 10$: MOVB F1-B(R11),@CURDOT-B(R11) : STORE BYTE
00 BB EO AB 80 0806 1387 RSB : RETURN
05 0808 1388 20$: MOVW F1-B(R11),@CURDOT-B(R11) : STORE WORD
00 BB EO AB D0 080C 1389 RSB : RETURN
05 0811 1390 30$: MOVL F1-B(R11),@CURDOT-B(R11) : STORE LONG
6B EO AB DA 0812 1391 RSB : RETURN
05 0816 1392 40$: MTPR F1-B(R11),CURDOT-B(R11) : SET VALUE IN PROCESSOR REGISTER
0817 1393 RSB :
0817 1394 .IFF : FALSE IF PROCESS VERSION
0817 1395 10$: : BYTE DEPOSIT
0817 1396 : START AND END ADDRESSES EQUAL
0817 1397 : SET WRITABLE, OLD PROT TO R2
0817 1398 : STORE BYTE
0817 1399 : RESTORE PROTECTION
0817 1400 RSB :
0817 1401 :
0817 1402 20$: ADDL3 #1,R4,R5 : WORD DEPOSIT, FORM END ADDRESS
0817 1403 BSBW SETWRT : SET WRITABLE
0817 1404 MOVW F1-B(R11),(R4) : STORE WORD
0817 1405 BSBW REPROT : RESTORE PROTECTION
0817 1406 RSB :
0817 1407 :
0817 1408 30$: ADDL3 #3,R4,R5 : LONGWORD DEPOSIT, FORM END ADDRESS
0817 1409 BSBW SETWRT : SET WRITABLE
0817 1410 MOVL F1-B(R11),(R4) : STORE LONG WORD
0817 1411 BSBW REPROT : RESTORE PROTECTION
0817 1412 RSB :
0817 1413 :
0817 1414 40$: SCHKRNLS B*DEPPREG,(AP) : PROCESSOR REGISTER
0817 1415 RSB : DEPOSIT IN PROCESSOR REGISTER
0817 1416 :
0817 1417 50$: : DEPOSIT IN ARBITRARY PROCESS
0817 1418 CASE CURTYPE-B(R11),TYPE=B,<- : SWITCH ON TYPE
0817 1419 60$,- : BYTE
```

```
0817 1420      70$.-      : WORD
0817 1421      80$>      : LONGWORD
0817 1422      :
0817 1423 60$:  RSB        : SET ADDRESS OF BYTE ROUTINE
0817 1424      PUSHAB W^DPBYTE
0817 1425 70$:  BRB        : SET ADDRESS OF WORD ROUTINE
0817 1426      PUSHAB W^DPWORD
0817 1427 80$:  BRB        : SET ADDRESS OF LONG ROUTINE
0817 1428 90$:  PUSHAB W^DPLONG
0817 1429      PUSHAB W^DPID-B(R11)
0817 1430      PUSHAB CURDOT-B(R11)
0817 1431      PUSHAB F1-B(R11)
0817 1432      PUSHAB #4
0817 1433      MOVL SP,R0
0817 1434      TSTL MFYFLG-B(R11)
0817 1435      BEQL 100$
0817 1436 100$: $CMKRNLS W^QGET,(R0)
0817 1437      ADDL #20,SP
0817 1438      RSB        : AND RETURN
0817 1439 DEPPREG: .WORD 0      : DEPOSIT INTO PROCESSOR REGISTER
0817 1440      MOVAB W^PREXC,(FP)
0817 1441      MTPR F1-B(R11),CURDOT-B(R11)
0817 1442      MOVL #1,R0
0817 1443      RET        : SET EXCEPTION HANDLER
0817 1444      : PLACE FIELD VALUE IN REG
0817 1445 PREXC: .WORD 0      : RETURN SUCESS
0817 1446      ADDL3 #4,8(AP),R1
0817 1447      MOVL (R1),12(FP)
0817 1448      MOVAB B^10$,16(FP)
0817 1449 10$:  MOVZWL #1,R0      : PROCESSOR REGISTER EXCEPTION HANDLER
0817 1450      RET        : POINT TO EXCEPTION FP
0817 1451      : SET AS RETURN FP
0817 1452      .ENDC      : SET RETURN ADDRESS
                        : SET NORMAL STATUS
                        : AND RETURN
```

```
09 6A 08 E1 0817 1454 .SBTTL EXECUTE - PERFORM COMMAND STRING
59 E0 AB D0 0817 1455 :
03 12 0817 1456 :
F981 31 0817 1457 :
05 05 0817 1458 EXECUTE:
0817 1459 BBC #V F1,(R10),10$ :
081B 1460 MOVL F1=B(R11),R9 :
081F 1461 BNEQ 10$ :
0821 1462 BRW 10$ :
0824 1463 10$: RSB :
0825 1464 :
: EXIT IF NO ADDRESS
: SET CHAR STRING
: NOT NULL
: SUPER RESET
: RETURN
```

- EXECUTIVE DEBUGGER
P - PROCESSOR REGISTER PREFIX

16-SEP-1984 02:02:16 VAX/VMS Macro V04-00
3-SEP-1984 02:07:42 [MP.SRC]XDELTA.MAR;1

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00 6A    OF    E2    0825    1466    .SBTTL P - PROCESSOR REGISTER PREFIX
05        0825    1467    :
0825    1468    :
0825    1469    :
0825    1470    PREG:    SET PROCESSOR REGISTER MODE
0825    1471    BBSS    #V_PMODE,(R10),10$    : PROCESSOR REGISTER MODE
0829    1472    10$:    RSB    : SET PROCESSOR REG FLAG
:    : RETURN

```



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082A 1474 .SBTTL PROCESS DEBUGGER INITIALIZATION
082A 1475
082A 1476 .IF DF,SW_PROCESS
082A 1477 SALUTE: .ASCIZ <CR><LF>/DELTA Version x2.1/<CR><LF> ;
082A 1478
082A 1479 TEST: ; START ADDRESS OF IMAGE ENTRY
082A 1480 XDT$START:: ; GLOBAL START ADDRESS FOR CLI DEBUG
082A 1481 .WORD 0
082A 1482 DELTA_START: ; START ADDRESS FOR DEBUGGER ENTRY
082A 1483 $WAKE_S ; NULL WAKE AND
082A 1484 $HIBER_S ; HIBERNATE TO GET SYNCHRONIZED
082A 1485 MOVAB TERMASK,TERMASKD+4 ; RELOCATE TERMINATOR MASK DESCR
082A 1486 MOVAB TTSTR,TTNAMD+4 ; RELOCATE DESCRIPTOR
082A 1487 MOVAB EXIHANDLE,EXIHADR
082A 1488 MOVAB EXITCODE,EXCODA ; RELOCATE EXIT HANDLER ARGS
082A 1489 CALLG (AP),B^INITCALL ; GENERATE CALL FRAME
082A 1490 RET
082A 1491
082A 1492 NOBRK: MOVL 4(AP),AP ; GET EXCEPTION ARGUMENT LIST
082A 1493 BRW EXCEPT+2 ; AND GOTO EXCEPTION HANDLER
082A 1494
082A 1495 INITCALL:
082A 1496 .WORD 0 ; ENTRY MASK
082A 1497 MOVAB W^CATCHALL,(FP) ; SET CATCHALL EXCEPTION HANDLER
082A 1498 $DCLEXH_S EXITBLK ; DECLARE USER MODE EXIT HANDLER
082A 1499 $CMKRNL_S W^SETEXC,(AP) ; SET EXCEPTION VECTORS
082A 1500 $SETEXV_S ADDRES=W^EXCEPT,
082A 1501 ;
082A 1502 ACMODE=#3,-
082A 1503 $SETEXV_S VECTOR=#0 ; SET PRIMARY FOR USER
082A 1504 ; ADDRES=W^CATCHALL,- ; SET LAST CHANCE HANDLER
082A 1505 ACMODE=#3,-
082A 1506 $ASSIGN_S VECTOR=#2 ; SPECIFY LAST CHANCE HANDLER
082A 1507 BLBS -R0,10$ ; ASSIGN DEVICE
082A 1508 RET ; CONTINUE IF SUCCESS
082A 1509 10$: MOVAB SALUTE,R4 ; ELSE EXIT WITH ERROR CODE IN R0
082A 1510 BSBW OUTZSTRING ; SET ADDRESS OF SALUTATION
082A 1511 BBS #CLISV_DBGEXCP,24(AP),NOBRK ; OUTPUT IT
082A 1512 ; BR IF LATER INVOCATION
082A 1513 CALLG (AP),B^20$ ; VIA $DEBUG COMMAND
082A 1514 RET ; CREATE TOP CALL FRAME
082A 1515 20$: .WORD 0 ; NULL ENTRY MASK
082A 1516 ADDL #4,4(AP) ; ADVANCE STARTING ADDRESS POINTER
082A 1517 MOVPSL -(SP) ; SAVE PSL
082A 1518 ADDL3 #2,24(AP),-(SP) ; FETCH CURRENT STARTING ADDRESS
082A 1519 MOVZWL #SS$_DEBUG,-(SP) ; SET EXCEPTION CODE
082A 1520 PUSHL #3 ; SIGNAL ARG COUNT
082A 1521 MOVL SP,R0 ; SAVE POINTER
082A 1522 MOVQ R0,-(SP) ; SAVE PHONY R0,R1
082A 1523 PUSHL #0 ; DEPTH
082A 1524 PUSHL FP ; FP
082A 1525 PUSHL #4 ; ARG COUNT
082A 1526 PUSHL SP ; POINTER TO MECH
082A 1527 PUSHL R0 ; POINTER TO SIGNAL
082A 1528 CALLS #2,W^EXCEPT ; SIGNAL PHONY EXCEPTION
082A 1529 ADDL #12,SP ; CLEAN BACK TO R0,R1
082A 1530 MOVQ (SP)+,R0 ; RESTORE R0,R1
```

```
082A 1531      ADDL      #8,SP      ; CLEAN BACK TO PC,PSL
082A 1532      REI              ; RETURN TO TARGET PROGRAM
082A 1533
082A 1534
082A 1535      SETEXC: .WORD      0      ; ENTRY MASK
082A 1536      $SETEXV_S      ADDRESS=B^EXCEPT, ;
082A 1537      PRVHND=KCOND,- ;
082A 1538      $SETEXV_S      ACMODE=#0      ; SET KERNEL
082A 1539      ADDRESS=W^CATCHALL,- ;
082A 1540      ACMODE=#0,- ; SET KERNEL MODE LAST CHANCE HANDLER
082A 1541      VECTOR=#2      ; SPECIFY LAST CHANCE VECTOR
082A 1542      ;-----
082A 1543      $SETEXV_S      ADDRESS=B^EXCEPT,- ;
082A 1544      PRVHND=ECOND,- ;
082A 1545      $SETEXV_S      ACMODE=#1      ; SET EXEC MODE EXCEPTION HANDLER
082A 1546      ADDRESS=W^CATCHALL,- ;
082A 1547      ACMODE=#1,- ; SET EXEC MODE LAST CHANCE HANDLER
082A 1548      VECTOR=#2      ; SPECIFY LAST CHANCE VECTOR
082A 1549      ;-----
082A 1550      $SETEXV_S      ADDRESS=B^EXCEPT,- ;
082A 1551      PRVHND=SCOND,- ;
082A 1552      $SETEXV_S      ACMODE=#2      ; SET SUPERVISOR MODE EXCEPTION HANDLER
082A 1553      ADDRESS=W^CATCHALL,- ;
082A 1554      ACMODE=#2,- ; SET SUPERVISOR LAST CHANCE HANDLER
082A 1555      VECTOR=#2      ; SPECIFY LAST CHANCE VECTOR
082A 1556      RET
082A 1557
082A 1558      EXCEPT: .WORD      0      ; EXCEPTION HANDLER ENTRY MASK
082A 1559      $SETEXV_S      ADDRESS=B^EXCEPT,- ;
082A 1560      ACMODE=#3,- ;
082A 1561      VECTOR=#0      ; RE-ESTABLISH USER PRIMARY VECTOR
082A 1562      ADDL3      #4,4(AP),R0      ; GET POINTER TO SIGNAL
082A 1563      MOVPSL      R1      ; GET CURRENT PSL
082A 1564      EXTZV      #PSL$V_CURMOD,#PSL$S_CURMOD,R1,R1 ;
082A 1565      BBSS      R1,DBGACTIVE,40$      ; BR IF ALREADY ACTIVE
082A 1566      CMPL      #SS$_TBIT,(R0)      ; IS IT TBIT?
082A 1567      BNEQ      10$      ; NO
082A 1568      5$:      BRW      XDELTBIT      ; YES, A TBIT
082A 1569      10$:     CMPL      #SS$_BREAK,(R0)      ; IS IT BREAKPOINT?
082A 1570      BNEQ      20$      ; NO
082A 1571      15$:     BRW      XDELBPT      ; YES, A BREAKPOINT
082A 1572      20$:     CMPL      #SS$_UNWINDING,(R0)      ; SOME OTHER EXCEPTION
082A 1573      BEQL      60$      ; IS IT UNWINDING
082A 1574      CMPL      #SS$_COMPAT,(R0)+ ; YES
082A 1575      BNEQ      30$      ; IS IT COMPATIBILITY MODE EXCEPT?
082A 1576      CMPL      #1,(R0)      ; NO
082A 1577      BEQL      15$      ; IS IT COMPATIBILITY BPT?
082A 1578      CMPL      #7,(R0)      ; YES
082A 1579      BEQL      5$      ; IS IT COMPATIBILITY TBIT?
082A 1580      CMPL      #SS$_DEBUG,-(R0)      ; YES
082A 1581      30$:     BNEQ      40$      ; IS IT DEBUG EXCEPTION?
082A 1582      BSBW      SAVE      ; NO
082A 1583      BRW      XDELDDBG      ; SAVE EVERYTHING
082A 1584      40$:     BBCC      R1,DBGACTIVE,50$      ; AND TREAT AS FUNNY BPT
082A 1585      CLRL      R0      ; UNEXPECTED EXCEPTION
082A 1586      50$:     ; CLEAR DEBUG ACTIVE
082A 1587      ; RETURN FALSE FOR RESIGNAL
```

```
082A 1588      RET
082A 1589 60$:  MOVL  #1,R0      ; IGNORE AND RESIGNAL
082A 1590      RET
082A 1591      .PAGE
082A 1592      .SBTTL  HANDLER FOR DEBUG EXCEPTIONS
082A 1593
082A 1594 DBGEXCEP:
082A 1595      .WORD  0
082A 1596      ADDL3  #4,8(AP),R1  ; POINT TO EXCEPTION FP
082A 1597      MOVL  FP,R0      ; INIT LINK FOR CALL FRAMES
082A 1598 10$:  CMPL  12(R0),(R1)  ; IS THIS THE LAST ONE?
082A 1599      BEQL  20$
082A 1600      MOVAB  B*30$,16(R0)  ; YES
082A 1601      MOVL  12(R0),R0      ; SET FOR RETURN
082A 1602      BRB   10$
082A 1603 20$:  MOVAB  XDELACV,16(R0)
082A 1604 30$:  RET
082A 1605
082A 1606 CATCHALL:
082A 1607      .WORD  0
082A 1608      MOVPSL R1
082A 1609      EXTZV  #PSL$V_CURMOD,#PSL$S_CURMOD,R1,R1  ; ISOLATE CURRENT MODE
082A 1610      BBSC  R1,DBGACTIVE,10$  ; MUST NOT BE DEBUGGER EXCEPTION
082A 1611      CLRL  R0
082A 1612      RET
082A 1613 10$:  BSBW  SAVE
082A 1614      ADDL3  #4,4(AP),R0  ; SAVE EVERYTHING
082A 1615      MOVL  (R0),R3      ; POINT TO EXCEPTION CODE
082A 1616      BSBW  CRLF
082A 1617      BSBW  OUTLONG
082A 1618      MOVAB  B*EXCMMSG,R4  ; GET IT
082A 1619      BSBW  OUTZSTRING  ; OUTPUT CR/LF
082A 1620      BRW   XDELDBG      ; OUTPUT EXCEPTION CODE
082A 1621 EXCMMSG: .ASCIZ  / EXCEPTION /  ; OUTPUT MESSAGE
082A 1622
082A 1623 EXIHANDLE:
082A 1624      .WORD  0
082A 1625      BITB  #15,DBGACTIVE
082A 1626      BEQL  10$
082A 1627      $CMKRNLS  CLREXV,(AP)
082A 1628      MOVL  -24(AP),R0
082A 1629      RET
082A 1630 10$:
082A 1631      MOVPSL  -(SP)
082A 1632      PUSHL  16(FP)
082A 1633      PUSHL  24(AP)
082A 1634      PUSHL  #3
082A 1635      PUSHR  #^M<R0,R1>
082A 1636      MOVQ  AP,-(SP)
082A 1637      PUSHL  #4
082A 1638      PUSHL  SP
082A 1639      PUSHAL  24(SP)
082A 1640      PUSHL  #2
082A 1641      MOVL  SP,AP
082A 1642      BSBW  SAVE
082A 1643      MOVAB  B*EXIMSG,R4
082A 1644      BSBW  OUTZSTRING  ; MECHANISM COUNT
                                ; POINTER TO MECHANISM
                                ; POINTER TO SIGNAL
                                ; SET AP FOR EXCEPTION
                                ; SAVE EVERYTHING
                                ; DISPLAY EXIT MESSAGE
                                ; OUTPUT TEXT
```

```
082A 1645      MOVL      SAVAP-B(R11),R3      : GET POINTER TO EXCEPTION ARGLIST
082A 1646      MOVL      4(R3),R3             : GET EXIT CODE ADDRESS
082A 1647      BSBW      OUTLONG              : DISPLAY IT
082A 1648      $DCLEXH_S  EXITBLK             : RE-ESTABLISH EXIT HANDLER
082A 1649      MOVPSL    -R1                  : GET CURRENT PSL
082A 1650      EXTZV     #PSL$V_CURMOD,#PSL$S_CURMOD,R1,R1 : GET CURRENT MODE
082A 1651      BBSS      R1,DBG$ACTIVE,20$     : SET DELTA ACTIVE FOR MODE
082A 1652 20$:      BRW      XD$ELDBG          :
082A 1653
082A 1654 EXIMSG: .ASCIZ  <CR><LF>/ EXIT /      :
082A 1655
082A 1656 CLREXV:      : CLR EXCEPTION VECTORS
082A 1657      .WORD      0                    : ENTRY MASK
082A 1658      $SETEXV_S  ADDRESS=@KCOND,-      :
082A 1659      ACMODE=#0                          :
082A 1660      $SETEXV_S  ADDRESS=@ECOND,-      :
082A 1661      ACMODE=#1                          :
082A 1662      $SETEXV_S  ADDRESS=@SCOND,-      :
082A 1663      ACMODE=#2                          :
082A 1664      RET                                :
082A 1665
082A 1666      .PAGE
082A 1667      .SBTTL  SETWRT - SET PAGES WRITABLE
082A 1668
082A 1669 SETWRT:      :
082A 1670      MOVAL      -(SP),R2              : ADDRESS FOR RETURN OF PROT
082A 1671      $CHKRNL_S  B^SETPRTK,(R2)         :
082A 1672      BLBS      -R0,10$                : CONTINUE IF NO ERROR
082A 1673      CALLG     (R2),B^SETPRTK         :
082A 1674 10$:      POPR      #^M<R2>          : RESTORE PROTECTION VALUE
082A 1675      RSB                                : RETURN
082A 1676
082A 1677 SETPRTK: .WORD      0                    :
082A 1678      MOVQ      R5,-(SP)                  : INADR, START AND END ADDRESSES
082A 1679      MOVL      SP,R1                    : ADDRESS OF INADR
082A 1680      $SETPRT_S  INADR=(R1),-          :
082A 1681      PROT=#PRTS$C_UW,-                : WRITABLE BY ALL
082A 1682      ACMODE=#0,-                      :
082A 1683      PRVPRT=(AP)                      : ADDRESS AT WHICH TO RETURN PROT
082A 1684      MOVL      #1,R0                    : ALWAYS SUCCESS
082A 1685      RET                                :
082A 1686
082A 1687 REPROT:      : RESTORE PROTECTION
082A 1688      RSB                                :
082A 1689      .PAGE
082A 1690      .SBTTL  FETCHP - FETCH DATA FROM ANOTHER PROCESS
082A 1691 FETCHP: CASE CURTYPE-B(R11),TYPE=B,<-
082A 1692      10$,-                                : 0 => BYTE
082A 1693      20$,-                                : 1 => WORD
082A 1694      30$>                                : 2 => LONG
082A 1695      RSB                                : UNKNOWN
082A 1696 10$:      PUSHAB  W^FPBYTE            : SET FOR BYTE FETCH
082A 1697      BRB      40$
082A 1698 20$:      PUSHAB  W^FPWORD            : SET FOR WORD FETCH
082A 1699      BRB      40$
082A 1700 30$:      PUSHAB  W^FPLONG            : SET FOR LONGWORD FETCH
082A 1701 40$:      PUSHL   PID-B(R11)          : P.D OF TARGET PROCESS
```



```
082A 1702 PUSHAB QUAN-B(R11) ; SET ADDRESS TO RETURN VALUE
082A 1703 PUSHL CURDOT-B(R11) ; AND ADDRESS OF VALUE
082A 1704 PUSHL #4 ; ARGUMENT COUNT
082A 1705 MOVL SP,R0 ; SAVE POINTER TO ARG LIST
082A 1706 $CMKRNLS W^QGET,(R0) ; Q AST FOR DATA FETCH
082A 1707 BLBC R0,50$ ; BR IF FAILED
082A 1708 $HIBERS ; WAIT FOR DATA TO RETURN
082A 1709 50$: ADDL #20,SP ; CLEAN STACK
082A 1710 RSB ; AND RETURN DATA
082A 1711 .PAGE
082A 1712 .SBTTL QGET - QUEUE AST TO GET DATA FROM ANOTHER PROCESS
082A 1713 :
082A 1714 : INPUTS: 04(AP) - LOCATION OF DATA
082A 1715 : 08(AP) - RETURN LOCATION
082A 1716 : 12(AP) - PID OF TARGET PROCESS
082A 1717 : 16(AP) - CODE SEGMENT POINTER
082A 1718 :
082A 1719 FP_ORIGPID=ACBSL_AST
082A 1720 FP_ADDR=ACBSL_ASTPRM
082A 1721 FP_VALUE=ACBSL_ASTPRM
082A 1722 FP_RETLOC=ACBSL_KAST+4
082A 1723 QGET: .WORD ^M<R2,R3,R4,R5> ; ENTRY MASK
082A 1724 MOVZWL #SS$ NONEXPR,R0 ; ASSUME BAD PIX
082A 1725 CMPW 12(AP),@#SCH$GL_MAXPIX ; CHECK PIX FOR LEGAL PROCESS
082A 1726 BGTR 10$ ; BR IF NOT
082A 1727 MOVZWL @16(AP),R1 ; GET SIZE OF CODE SEGMENT
082A 1728 MOVAB IRP$C_LENGTH(R1),R1 ; ADD SIZE OF PACKET DATA
082A 1729 JSB @#EXES$ALLOCBUF ; ALLOCATE BUFFER TO CONTAIN CODE
082A 1730 BLBC R0,10$ ; BRANCH IF NONE
082A 1731 MOVL R2,R5 ; SAVE ADDRESS OF PACKET
082A 1732 PCBSL_PID(R4),FP_ORIGPID(R5) ; SET PID FOR RETURN
082A 1733 MOVB #^X80,ACBSB_RMOD(R5) ; SET FOR SPECIAL KERNEL AST
082A 1734 MOVAB ACBSL_KAST+8(R5),ACBSL_KAST(R5) ; SET ADDRESS FOR AST
082A 1735 MOVL 4(AP),FP_ADDR(R5) ; SET ADDRESS FOR FETCH
082A 1736 MOVL 8(AP),FP_RETLOC(R5) ; AND ADDRESS OF RETURN LOCATION
082A 1737 MOVL 16(AP),R0 ; GET ADDRESS OF CODE SEGMENT
082A 1738 MOVL 12(AP),ACBSL_PID(R5) ; SET TARGET PID
082A 1739 PUSHR #^M<R0,R1,R2,R3,R4,R5> ; SAVE REGS FOR MOV
082A 1740 MOVC3 (R0)+,(R0),ACBSL_KAST+8(R5) ; COPY CODE SEGMENT TO BUFFER
082A 1741 POPR #^M<R0,R1,R2,R3,R4,R5> ; RESTORE REGISTERS
082A 1742 MOVZBL #PRI$ TICOM,R2 ; SET PRIORITY INCREMENT CLASS
082A 1743 JSB @#SCH$QAST ; QUEUE AST FOR TARGET
082A 1744 10$: RET ; RETURN TO ORIGINAL MODE
082A 1745 :
082A 1746 .SBTTL FPBYTE - FETCH BYTE FROM PROCESS
082A 1747 FPBYTE: .WORD 90$-,-2 ; SIZE OF CODE SEGMENT
082A 1748 IFNORD #1,@FP_ADDR(R5),10$ ; BRANCH IF NOT READABLE
082A 1749 MOVB @FP_ADDR(R5),FP_VALUE(R5) ; GET VALUE
082A 1750 10$: MOVL FP_ORIGPID(R5),ACBSL_PID(R5) ; SET PID FOR RETURN AST
082A 1751 MOVB #^X80,ACBSB_RMOD(R5) ; SET FOR KAST AGAIN
082A 1752 MOVAB B^20$,ACBSL_KAST(R5) ; SET NEW AST ADDRESS
082A 1753 MOVZBL #PRI$ TICOM,R2 ; SET PRIORITY INCREMENT CLASS
082A 1754 JMP @#SCH$QAST ; QUEUE RETURN AST
082A 1755 20$: IFNOWRT #1,@FP_RETLOC(R5),30$ ; IF NOT WRITABLE THEN SKIP IT
082A 1756 MOVB FP_VALUE(R5),@FP_RETLOC(R5) ; RETURN VALUE
082A 1757 30$: MOVL ACBSL_PID(R5),R1 ; GET PID FOR WAKE
082A 1758 SETIPL #IPL$_SYNCH ; RAISE TO SYNCH
```

```
082A 1759 JSB @#SCH$WAKE ; WAKE PROCESS
082A 1760 SETIPL #IPL$-ASTDEL ; LOWER IPL
082A 1761 MOVL R5,R0 ; SET ADDRESS FOR RELEASE
082A 1762 JMP @#EXES$DEANONPAGED ; FREE BLOCK AND EXIT
082A 1763 90$: ; END OF CODE SEGMENT
082A 1764
082A 1765 .PAGE
082A 1766 .SBTTL DPBYTE - DEPOSIT BYTE TO PROCESS
082A 1767 DPBYTE: .WORD 90$-.-2 ; SIZE OF CODE SEGMENT
082A 1768 20$: IFNOWRT #1,@FP_RETLOC(R5),30$ ; IF NOT WRITABLE THEN SKIP IT
082A 1769 MOVB FP_VALUE(R5),@FP_RETLOC(R5) ; RETURN VALUE
082A 1770 30$: MOVL R5,R0 ; SET ADDRESS FOR RELEASE
082A 1771 JMP @#EXES$DEANONPAGED ; FREE BLOCK AND EXIT
082A 1772 90$: ; END OF CODE SEGMENT
082A 1773
082A 1774 .PAGE
082A 1775 .SBTTL FPWORD - FETCH WORD FROM PROCESS
082A 1776 FPWORD: .WORD 90$-.-2 ; SIZE OF CODE SEGMENT
082A 1777 IFNORD #2,@FP_ADDR(R5),10$ ; BRANCH IF NOT READABLE
082A 1778 MOVW @FP_ADDR(R5),FP_VALUE(R5) ; GET VALUE
082A 1779 10$: MOVL FP_ORIGPID(R5),ACBSL_PID(R5) ; SET PID FOR RETURN AST
082A 1780 MOVB #^X80,ACBSB_RMOD(R5) ; SET FOR KAST AGAIN
082A 1781 MOVAB B^20$,ACBSL_KAST(R5) ; SET FOR NEW AST ADDRESS
082A 1782 MOVZBL #PRI$ T1COM,R2 ; SET PRIORITY INCREMENT CLASS
082A 1783 JMP @#SCH$QAST ; QUEUE RETURN AST
082A 1784 20$: IFNOWRT #2,@FP_RETLOC(R5),30$ ; IF NOT WRITABLE THEN SKIP IT
082A 1785 MOVW FP_VALUE(R5),@FP_RETLOC(R5) ; RETURN VALUE
082A 1786 30$: MOVL ACBSL_PID(R5),R1 ; GET PID FOR WAKE
082A 1787 SETIPL #IPL$-SYNCH ; RAISE TO SYNCH
082A 1788 JSB @#SCH$WAKE ; WAKE PROCESS
082A 1789 SETIPL #IPL$-ASTDEL ; LOWER IPL
082A 1790 MOVL R5,R0 ; SET ADDRESS FOR RELEASE
082A 1791 JMP @#EXES$DEANONPAGED ; FREE BLOCK AND EXIT
082A 1792 90$: ; END OF CODE SEGMENT
082A 1793
082A 1794 .PAGE
082A 1795 .SBTTL DPWORD - DEPOSIT WORD TO PROCESS
082A 1796 DPWORD: .WORD 90$-.-2 ; SIZE OF CODE SEGMENT
082A 1797 20$: IFNOWRT #2,@FP_RETLOC(R5),30$ ; IF NOT WRITABLE THEN SKIP IT
082A 1798 MOVW FP_VALUE(R5),@FP_RETLOC(R5) ; RETURN VALUE
082A 1799 30$: MOVL R5,R0 ; SET ADDRESS FOR RELEASE
082A 1800 JMP @#EXES$DEANONPAGED ; FREE BLOCK AND EXIT
082A 1801 90$: ; END OF CODE SEGMENT
082A 1802
082A 1803 .PAGE
082A 1804 .SBTTL FPLONG - FETCH LONG FROM PROCESS
082A 1805 FPLONG: .WORD 90$-.-2 ; SIZE OF CODE SEGMENT
082A 1806 IFNORD #4,@FP_ADDR(R5),10$ ; BRANCH IF NOT READABLE
082A 1807 MOVL @FP_ADDR(R5),FP_VALUE(R5) ; GET VALUE
082A 1808 10$: MOVL FP_ORIGPID(R5),ACBSL_PID(R5) ; SET PID FOR RETURN AST
082A 1809 MOVB #^X80,ACBSB_RMOD(R5) ; SET FOR KAST AGAIN
082A 1810 MOVAB B^20$,ACBSL_KAST(R5) ; SET NEW KAST ADDRESS
082A 1811 CLRL R2 ; NULL PRIO INCR
082A 1812 JMP @#SCH$QAST ; QUEUE RETURN AST
082A 1813 20$: IFNOWRT #4,@FP_RETLOC(R5),30$ ; IF NOT WRITABLE THEN SKIP IT
082A 1814 MOVL FP_VALUE(R5),@FP_RETLOC(R5) ; RETURN VALUE
082A 1815 30$: MOVL ACBSL_PID(R5),R1 ; GET PID FOR WAKE
```

```
082A 1816      SETIPL #IPL$ SYNCH      : RAISE TO SYNCH
082A 1817      JSB     @#SCH$WAKE      : WAKE PROCESS
082A 1818      SETIPL #IPL$ ASTDEL     : LOWER IPL
082A 1819      MOVL   R5,R0           : SET ADDRESS FOR RELEASE
082A 1820      JMP     @#EXES$DEANONPAGED : FREE BLOCK AND EXIT
082A 1821 90$:                                     : END OF CODE SEGMENT
082A 1822
082A 1823      .PAGE
082A 1824      .SBTTL DPLONG - DEPOSIT LONGWORD TO PROCESS
082A 1825 DPLONG: .WORD 90$-,-2      : SIZE OF CODE SEGMENT
082A 1826 20$:  IFNOWRT #4,@FP_RETLOC(R5),30$ : IF NOT WRITABLE THEN SKIP IT
082A 1827      MOVL   FP_VALUE(R5),@FP_RETLOC(R5) : RETURN VALUE
082A 1828 30$:  MOVL   R5,R0           : SET ADDRESS FOR RELEASE
082A 1829      JMP     @#EXES$DEANONPAGED : FREE BLOCK AND EXIT
082A 1830 90$:                                     : END OF CODE SEGMENT
082A 1831 DELEND:                                     :
082A 1832      .ENDC
082A 1      :
082A 2      : NORMAL END STATEMENT WITHOUT START ADDRESS
082A 3      : USED TO ASSEMBLE XDELTA FOR EXEC DEBUGGING.
082A 4      :
082A 5      .END
```


XDELTA
Symbol table

- EXECUTIVE DEBUGGER

C 10

16-SEP-1984 02:02:16 VAX/VMS Macro V04-00 Page 54
3-SEP-1980 13:47:15 DISK\$VMSMASTER:[MP.SRC]END.MAR;1 (1)

ADD	0000024A	R	02
ASTEN	000000B8	R	02
B	00000058	R	02
BLANK	00000431	R	02
BMSG	000006EA	R	02
BRKADR	= 000000B8	R	02
BRKCOM	= 00000100	R	02
BRKDSP	= 000000E0	R	02
BRKOP	= 000000DB	R	02
BRKPOINT	000004D7	R	02
BSLSH	= 0000005C		
CLR_730	00000607	R	02
CLR_750	00000607	R	02
CLR_780	00000602	R	02
CLR_END	0000060A	R	02
COLON	0000057F	R	02
COMMA	0000029B	R	02
CONTEXT	00000000	R	02
CONTEXTSZ	= 000000BC		
CR	= 0000000D		
CRLF	000003B4	R	02
CURDOT	00000058	R	02
CURTYPE	00000056	R	02
DCOM	0000019A	R	02
DEPOSIT	000007F1	R	02
DIV	00000246	R	02
DOT	0000058F	R	02
DQUOTE	0000024E	R	02
DTYPE	00000055	R	02
ENDEXPR	0000021F	R	02
ENDFIELD	0000029E	R	02
EQL1	0000046F	R	02
EQUALS	00000468	R	02
ERR2	000004AB	R	02
ERR3	0000060A	R	02
ERR4	00000281	R	02
ERROR	0000019E	R	02
ESCAP	00000456	R	02
EXESACVIOLAT	*****	X	02
EXESBREAK	*****	X	02
EXESGB_CPUTYPE	*****	X	02
EXESROPRAND	*****	X	02
EXESTBIT	*****	X	02
EXECUTE	00000817	R	02
F1	00000038	R	02
F2	0000003C	R	02
F3	00000040	R	02
F4	00000044	R	02
F5	00000048	R	02
FCTR	00000054	R	02
FETCH	000002B9	R	02
GETBPTX	000007CD	R	02
GETCHAR	000003BE	R	02
GETCMD	0000074C	R	02
GETSCB	000006D8	R	02
GLOBL	0000020A	R	02
GO	00000575	R	02

HIGH	00000210	R	02
INBUF	00000004	R	02
INFLD	00000206	R	02
INISBRK	*****	X	02
LBRACKET	000004B2	R	02
LF	= 0000000A		
LINEFEED	000002F2	R	02
LOCOUT	000002F9	R	02
LOCP	00000465	R	02
LOCPROMPT	000002F7	R	02
MCHK	000005F0	R	02
MCHKSAV	00000164	R	02
MFYFLG	0000004C	R	02
MFYFLGS	00000589	R	02
MMG\$PAGEFAULT	*****	X	02
MODES	000004AE	R	02
MUL	C0000242	R	02
NBRK	= 00000008		
NEGATE	0000043A	R	02
NEXTDOT	000002DF	R	02
NEXTLOC	000002F5	R	02
NEXTP	000001B5	R	02
NMODES	= 00000004		
NPRIM	= 0000002A		
NSEC	= 00000007		
NTERM	= 00000008		
OPEN	00000256	R	02
OPER	00000057	R	02
OPERATOR	00000431	R	02
OPERBAS	= 00000012		
OUTB	= 00000006		
OUTBB	000002EF	R	02
OUTBSLSH	00000388	R	02
OUTBUF	00000060	R	02
OUTCHAR	00000391	R	02
OUTCOM	00000364	R	02
OUTCR	= 00000004		
OUTDIGIT	0000035D	R	02
OUTER	00000192	R	02
OUTLONG	00000361	R	02
OUTPUT	000002FE	R	02
OUTPUTA	00000324	R	02
OUTR8	0000038E	R	02
OUTSPACE	000003AF	R	02
OUTZBUF	0000037A	R	02
OUTZSTRING	0000037E	R	02
PFNSAB_STATE	*****	X	02
PFNSAB_TYPE	*****	X	02
PFNSAL_BAK	*****	X	02
PFNSAL_PTE	*****	X	02
PFNSAW_REFCNT	*****	X	02
PFNSAW_SUPVBN	*****	X	02
PFNSAX_BLINK	*****	X	02
PFNSAX_FLINK	*****	X	02
PID	00000050	R	02
PR\$_IPL	= 00000012		
PR\$_KSP	= 00000000		

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TERM			
UNBRK		00000181	R 02
VALI		00000795	R 02
VALR		000005B1	R 02
VALUE		000005AE	R 02
V_ASCII		000005A6	R 02
V_ATBRK	=	00000001	
V_F1	=	00000004	
V_F2	=	00000008	
V_F3	=	00000009	
V_F4	=	0000000A	
V_F5	=	0000000B	
V_INFIELD	=	0000000C	
V_NEGATE	=	00000002	
V_OPEN	=	00000007	
V_PREG	=	00000000	
V_PMODE	=	0000001F	
V_RUB	=	0000000F	
V_TBIT	=	00000006	
V_TBITOK	=	00000003	
XDELACV		00000005	
XDELBPT		000005F0	R 02
XDELDDBG		000006F4	RG 02
XDELIBRK		0000077B	R 02
XDELTBIT		0000008C	RG 02
XDEL_LOADBASE		00000764	RG 02
XDSS\$GL_XESTRING		00000130	RG 02
XDSS\$GL_XFSTRING		0000015C	RG 02
XDSS\$GT_WORD_PFN		00000160	RG 02
XREG		*****	X 02
XREGV		000005E2	R 02
XSET		00000124	R 02
		000005D0	R 02

[illegible]

+-----+
! Psect synopsis !
+-----+

PSECT name	Allocation	PSECT No.	Attributes
ABS	00000000 (0.)	00 (0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
\$ABSS	00000000 (0.)	01 (1.)	NOPIC USR CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE
Z\$DEBUGXDELTA	0000082A (2090.)	02 (2.)	NOPIC USR CON REL LCL NOSHR EXE RD WRT NOVEC LONG

+-----+
! Performance indicators !
+-----+

Phase	Page faults	CPU Time	Elapsed Time
Initialization	38	00:00:00.11	00:00:00.65
Command processing	124	00:00:01.02	00:00:04.74
Pass 1	405	00:00:15.16	00:00:44.35
Symbol table sort	0	00:00:02.05	00:00:03.57
Pass 2	338	00:00:05.33	00:00:12.81
Symbol table output	24	00:00:00.23	00:00:00.68
Psect synopsis output	2	00:00:00.02	00:00:00.02
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	933	00:00:23.92	00:01:06.82

The working set limit was 1950 pages.
87249 bytes (171 pages) of virtual memory were used to buffer the intermediate code.
There were 70 pages of symbol table space allocated to hold 1237 non-local and 91 local symbols.
1842 source lines were read in Pass 1, producing 18 object records in Pass 2.
24 pages of virtual memory were used to define 23 macros.

+-----+
! Macro library statistics !
+-----+

Macro library name	Macros defined
\$255\$DUA28:[MP.OBJ]MP.MLB;1	10
\$255\$DUA28:[SYS.OBJ]LIB.MLB;1	10
\$255\$DUA28:[SYSLIB]STARLET.MLB;2	8
TOTALS (all libraries)	28

1396 GETS were required to define 28 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:XDELTA/OBJ=OBJ\$:XDELTA MSRC\$:MPPREFIX/UPDATE=(ENH\$:MPPREFIX)+MSRC\$:XDELTA/UPDATE=(ENH\$:XDELTA)+MSRC\$:END/UPDATE=(ENH\$

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AH-BT13A-SE
VAX/VMS V4.0

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